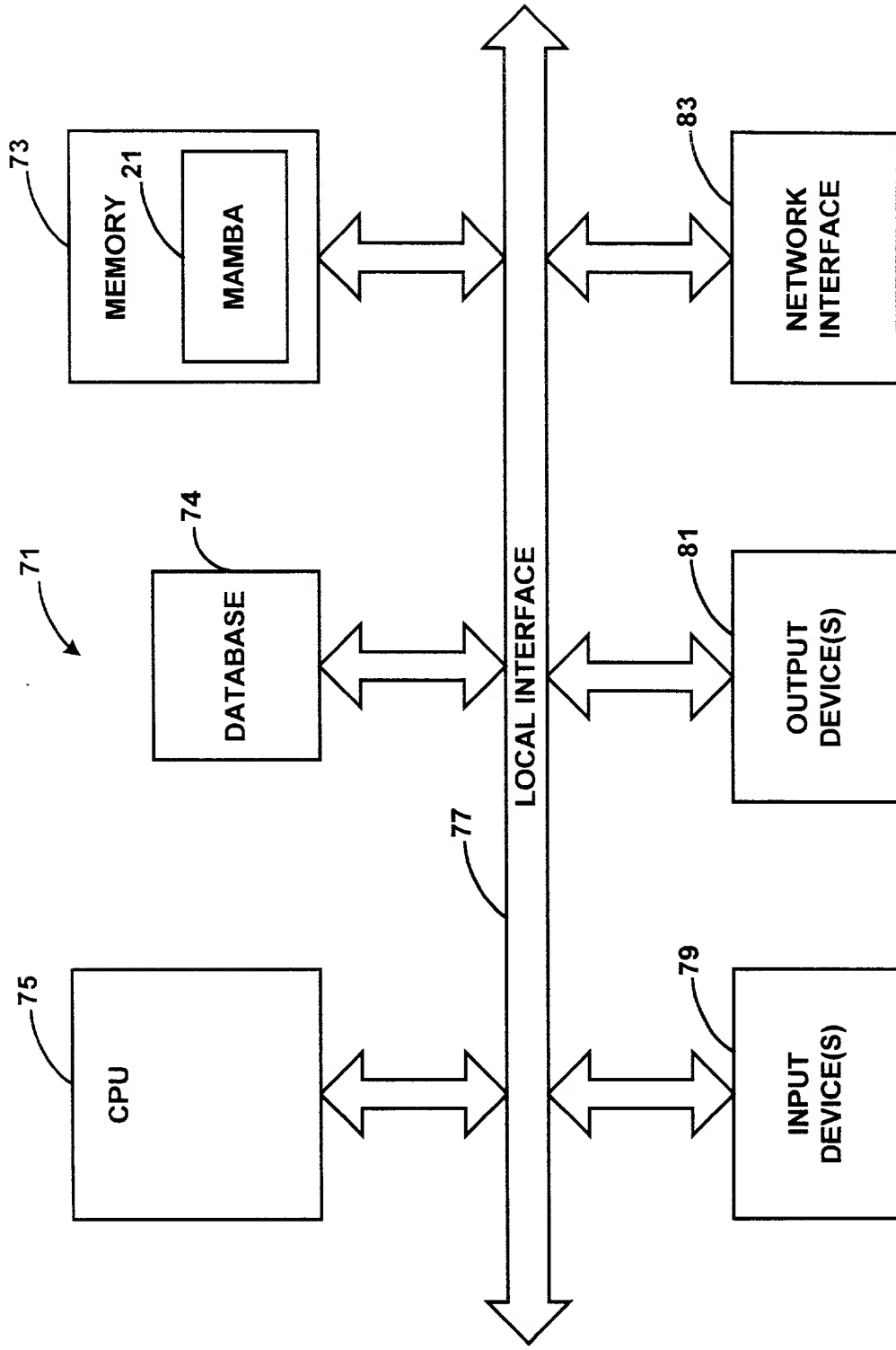


FIG. 1



**FIG. 2A**

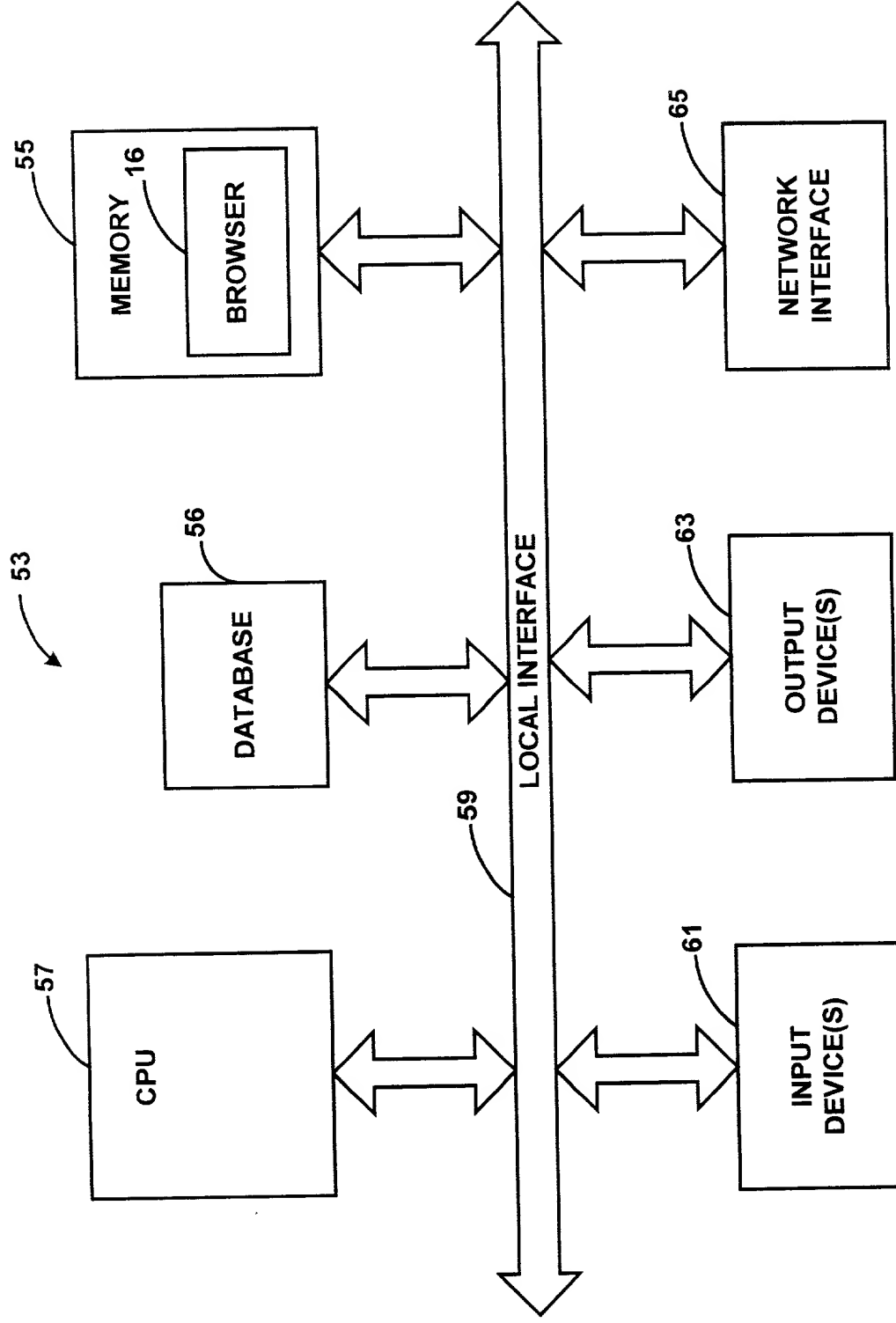
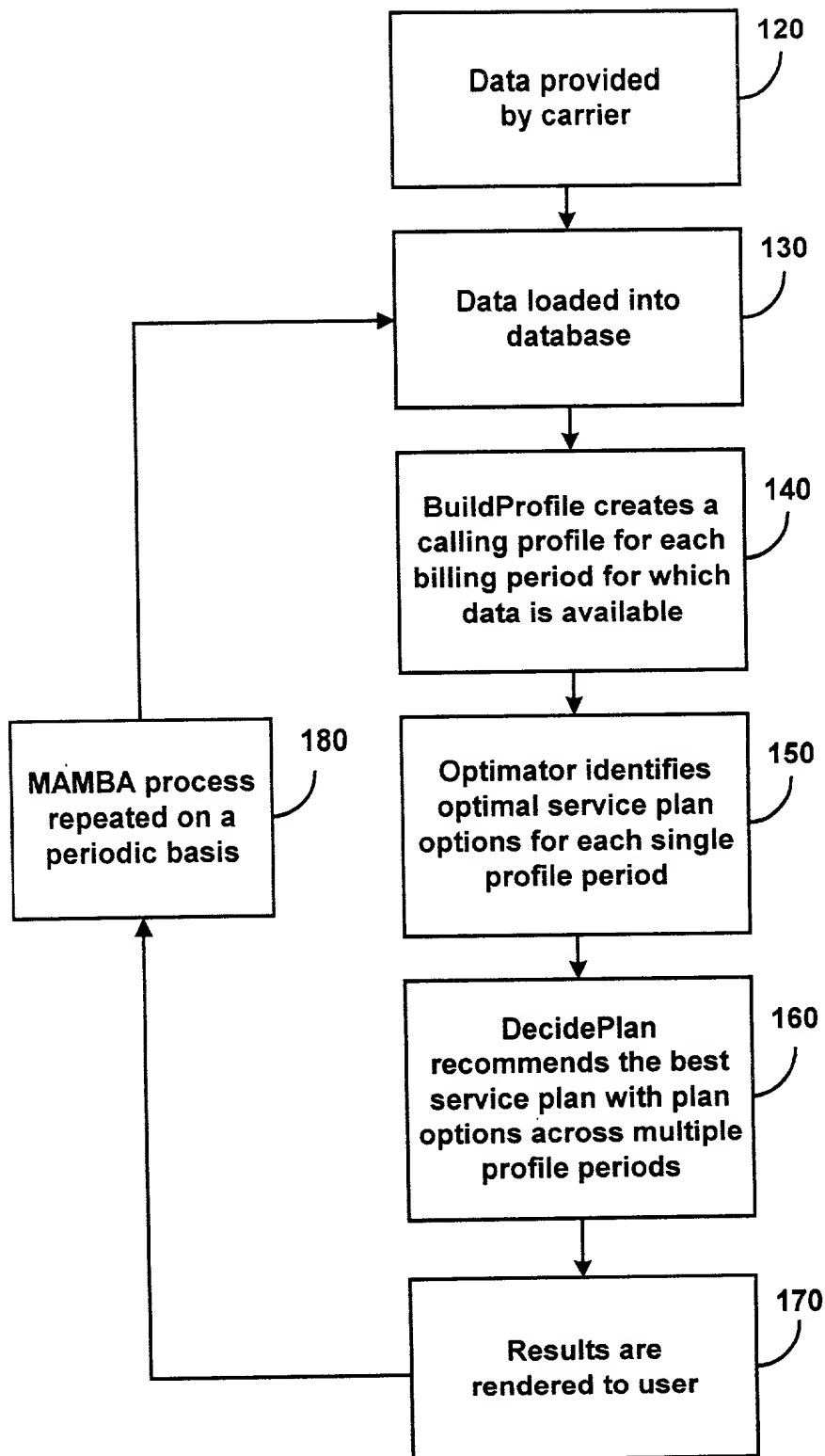


FIG. 2B



**FIG. 3**

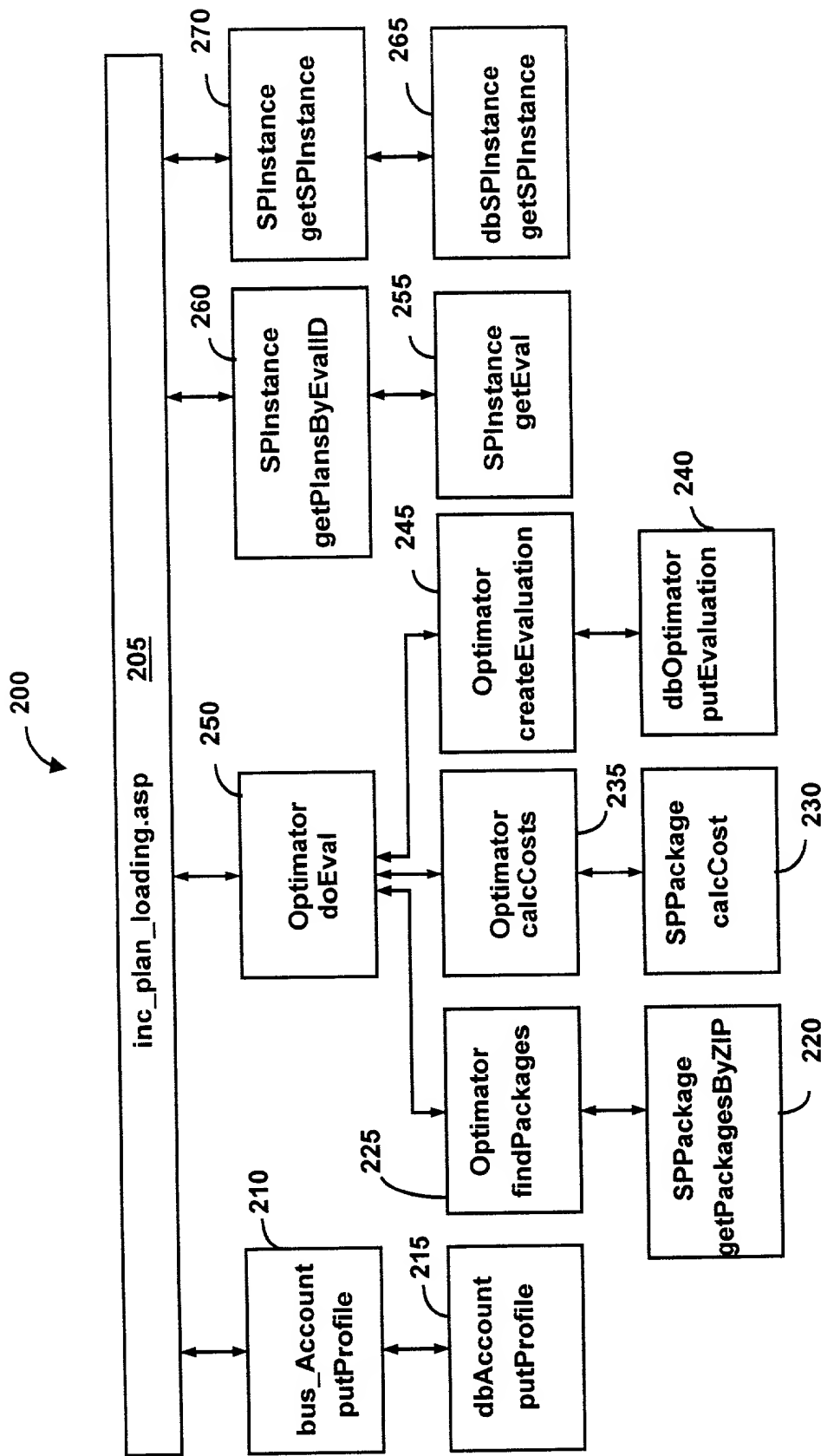


FIG. 4

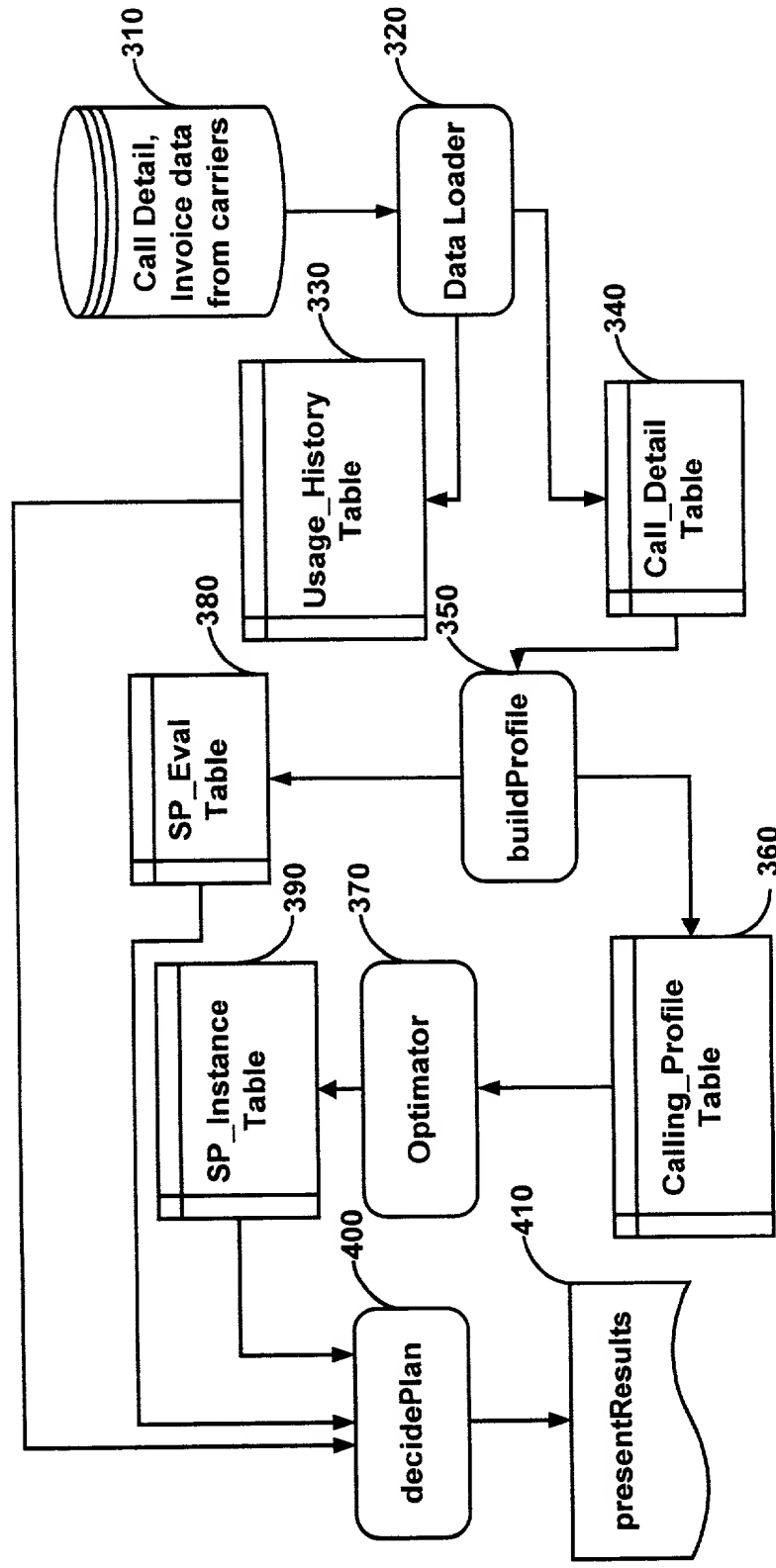
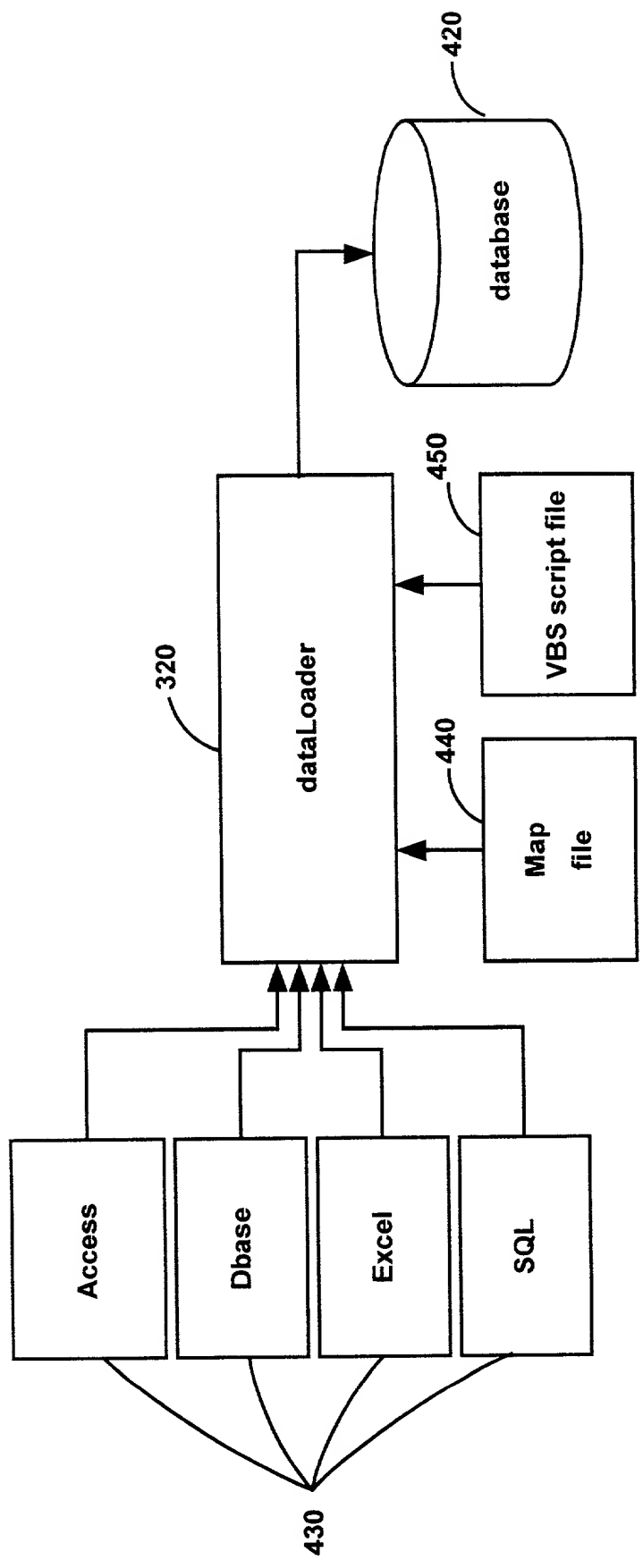
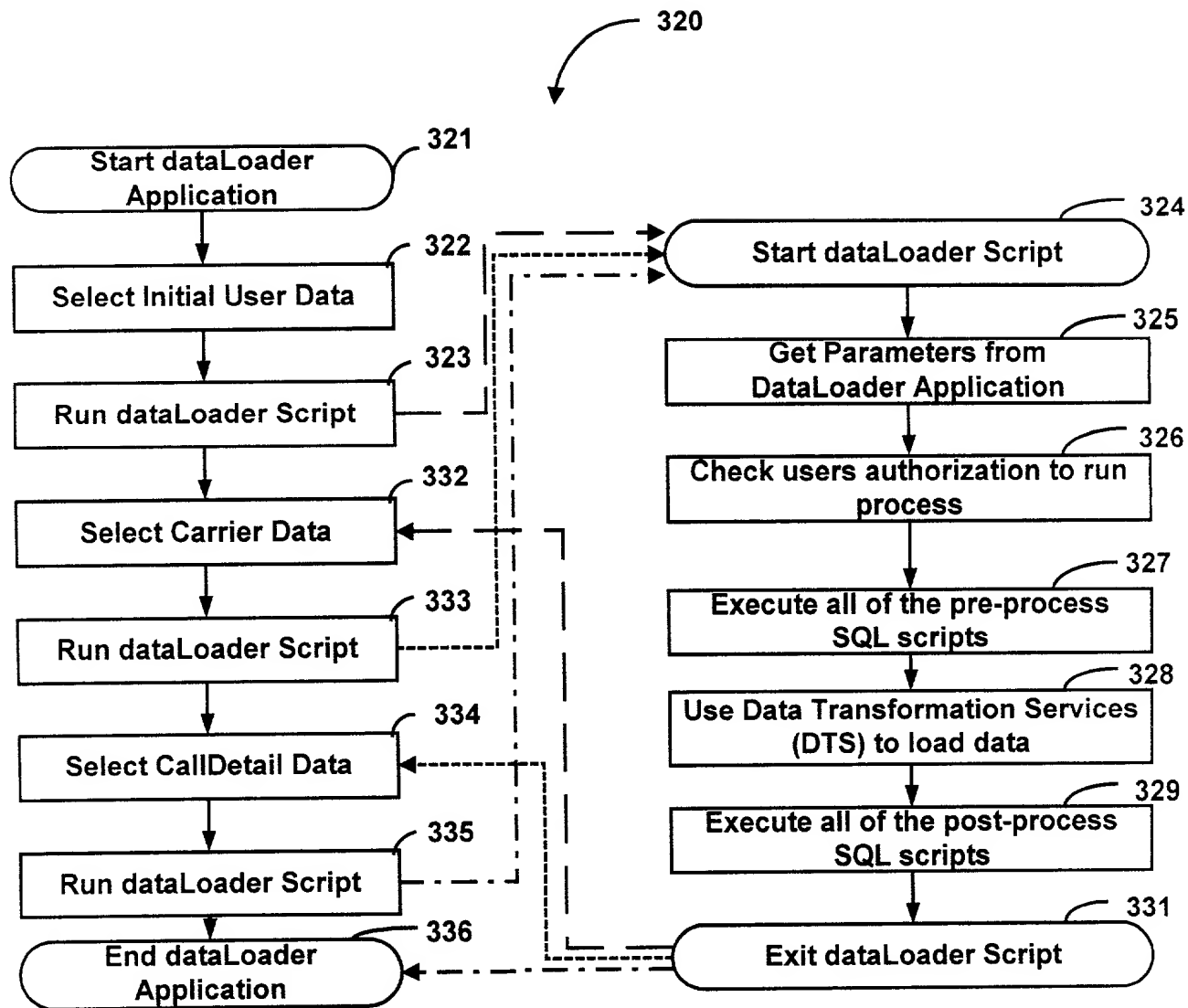


FIG. 5

FIG. 6 is a block diagram of a data loader system.

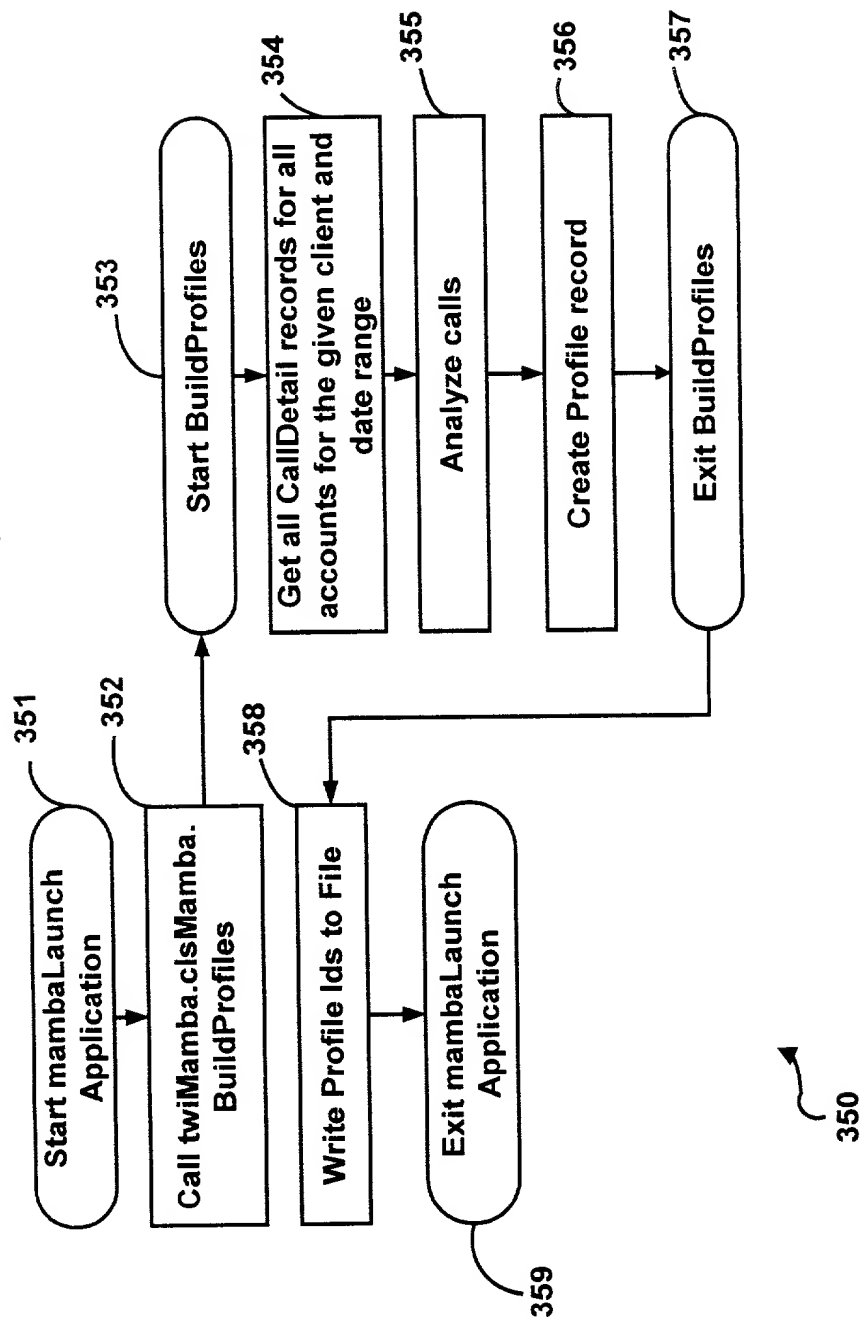


**FIG. 6**

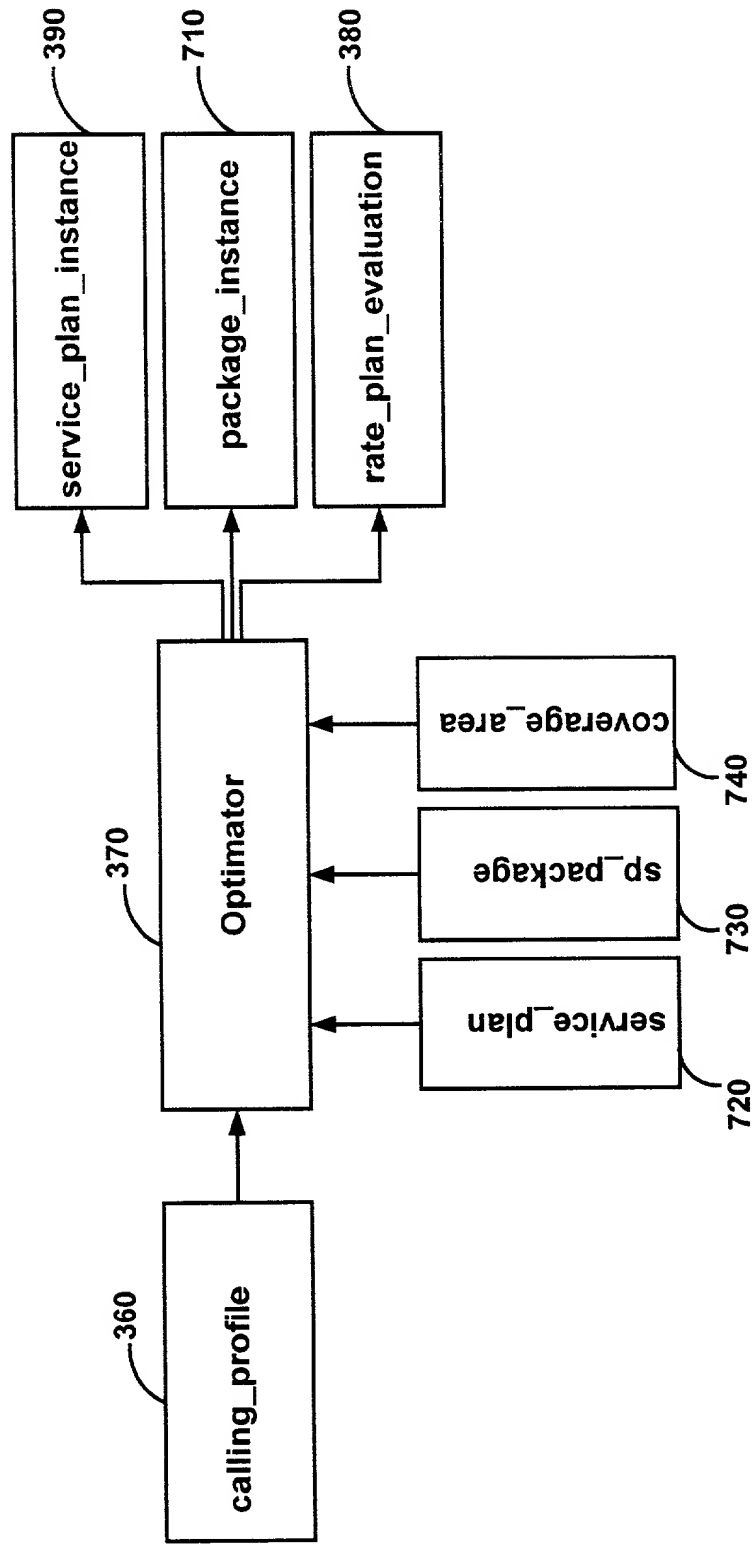


**FIG. 7**

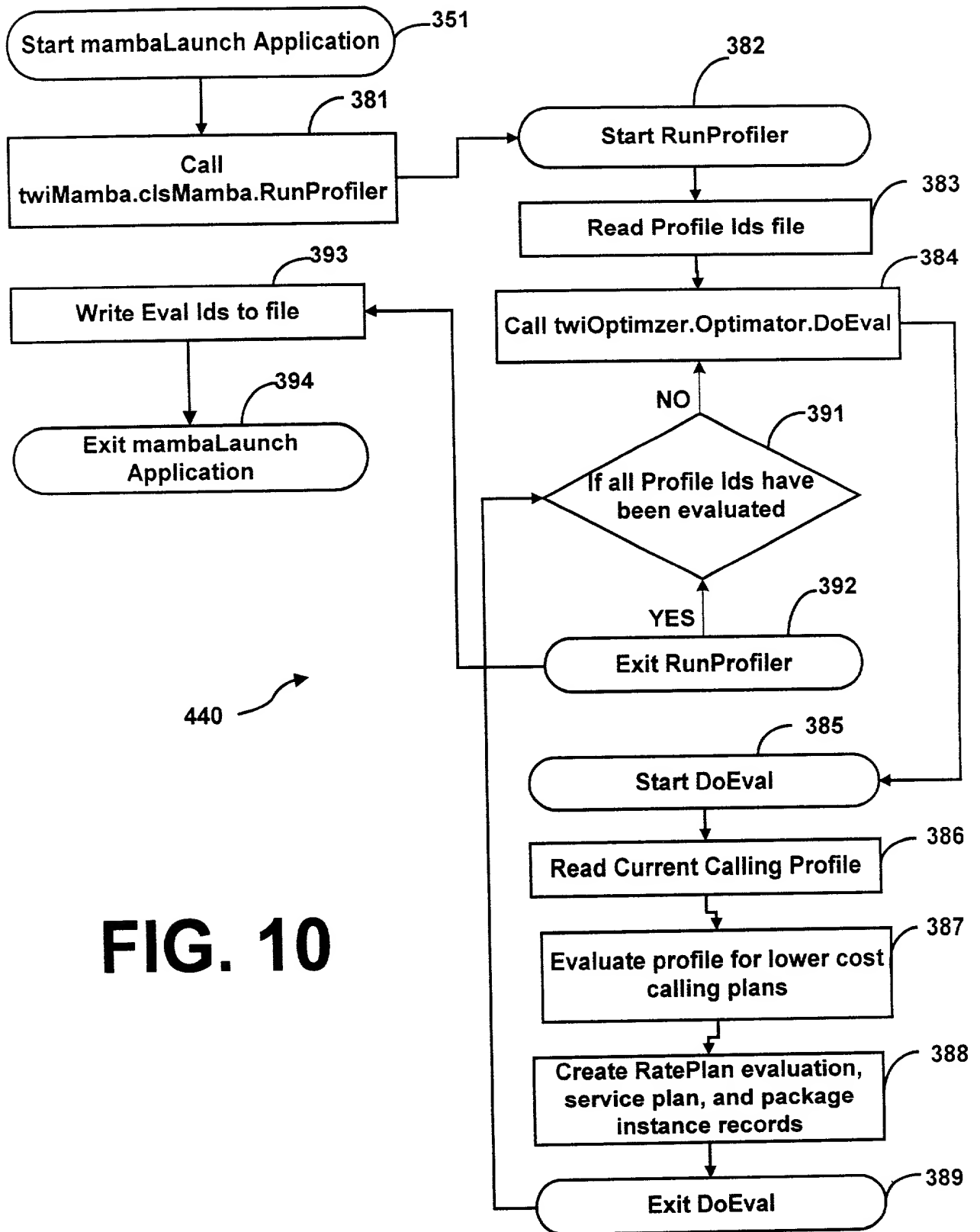




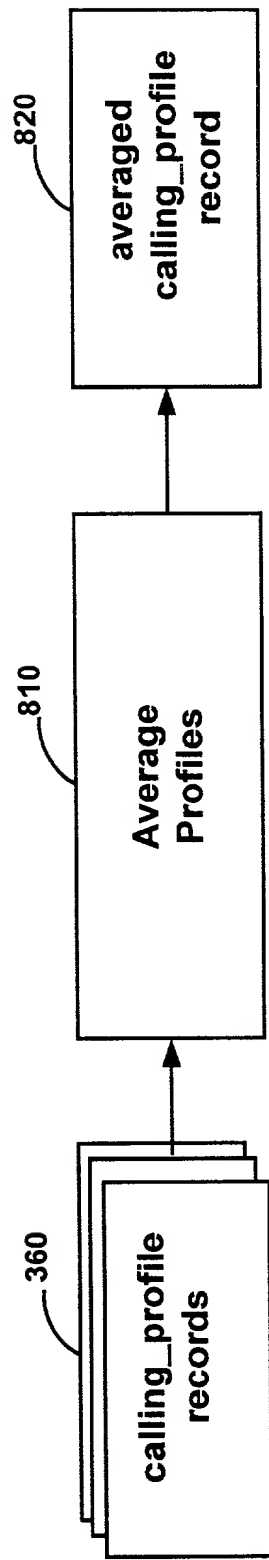
**FIG. 8**



**FIG. 9**



**FIG. 10**



**FIG. 11**

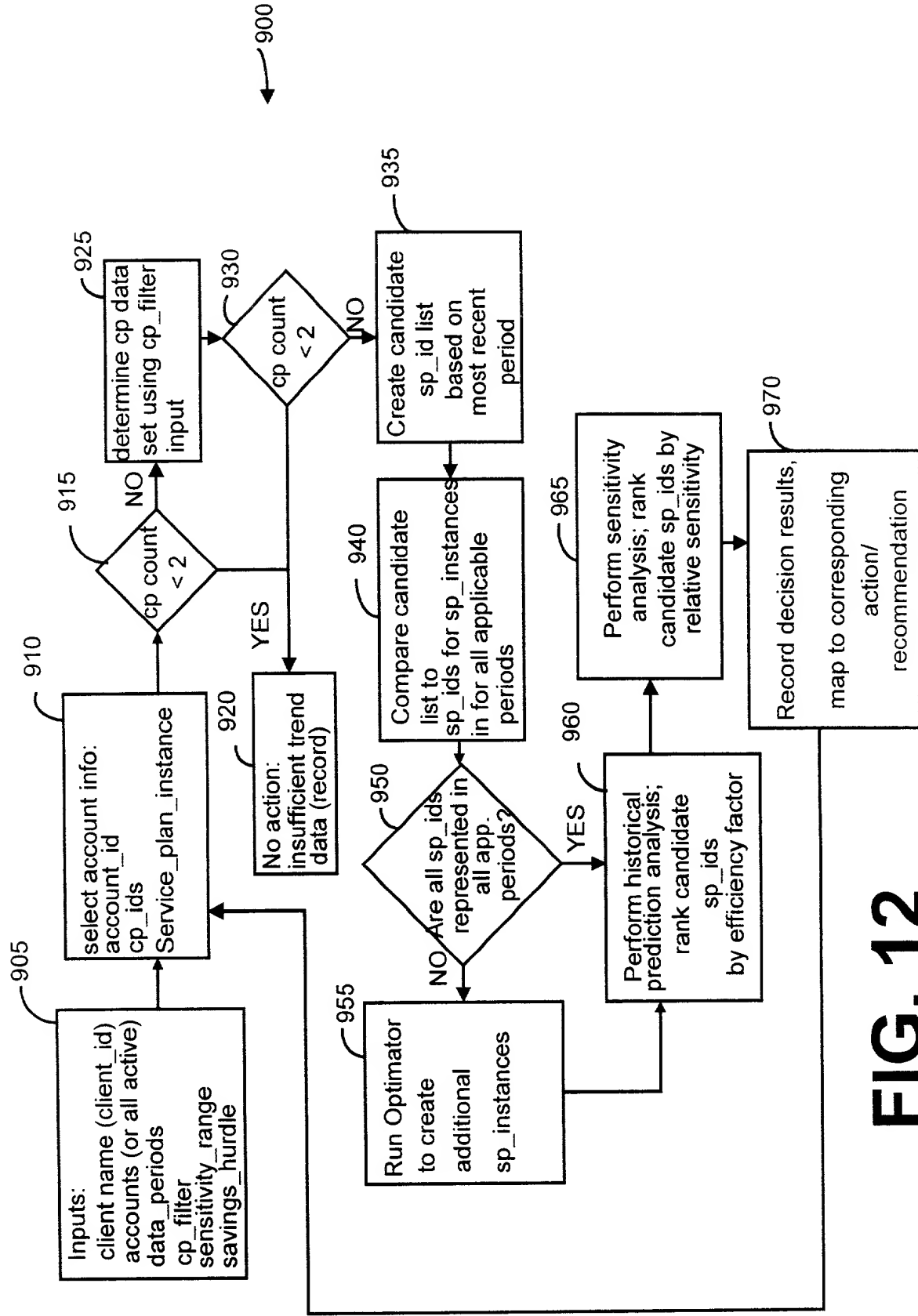


FIG. 12

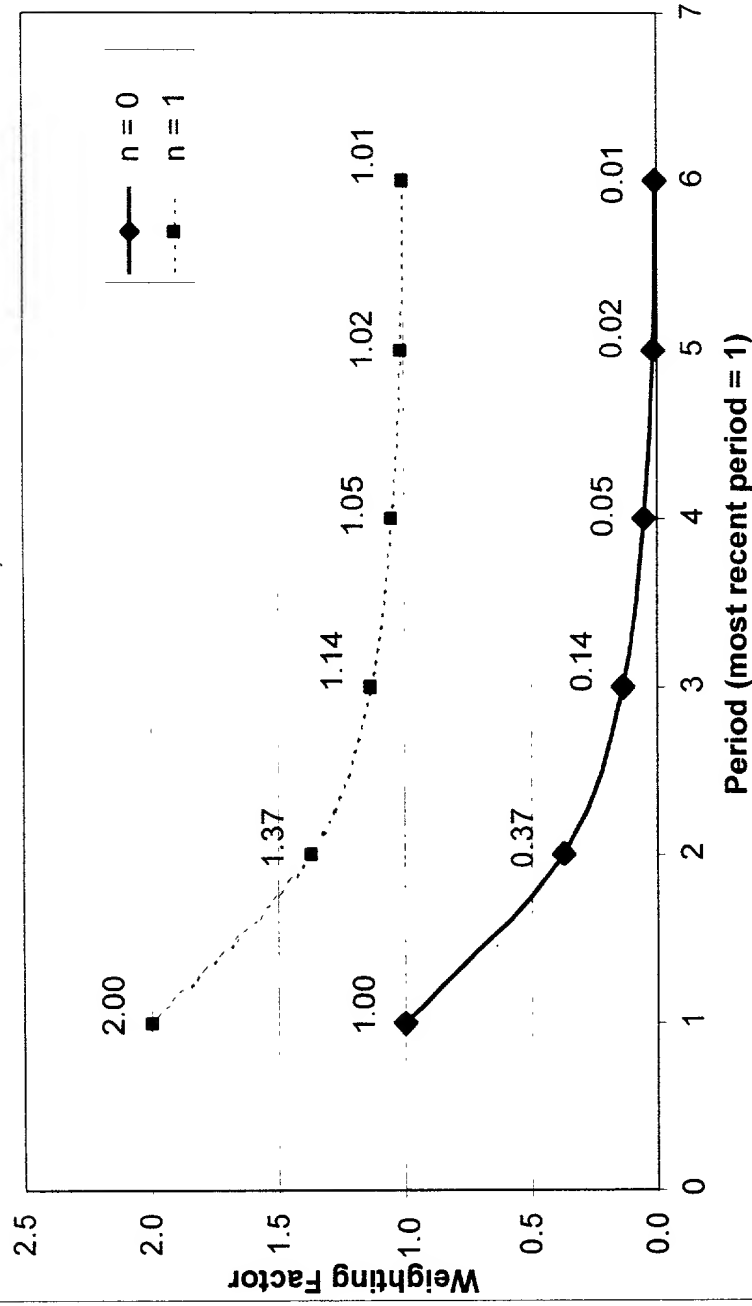


FIG. 13

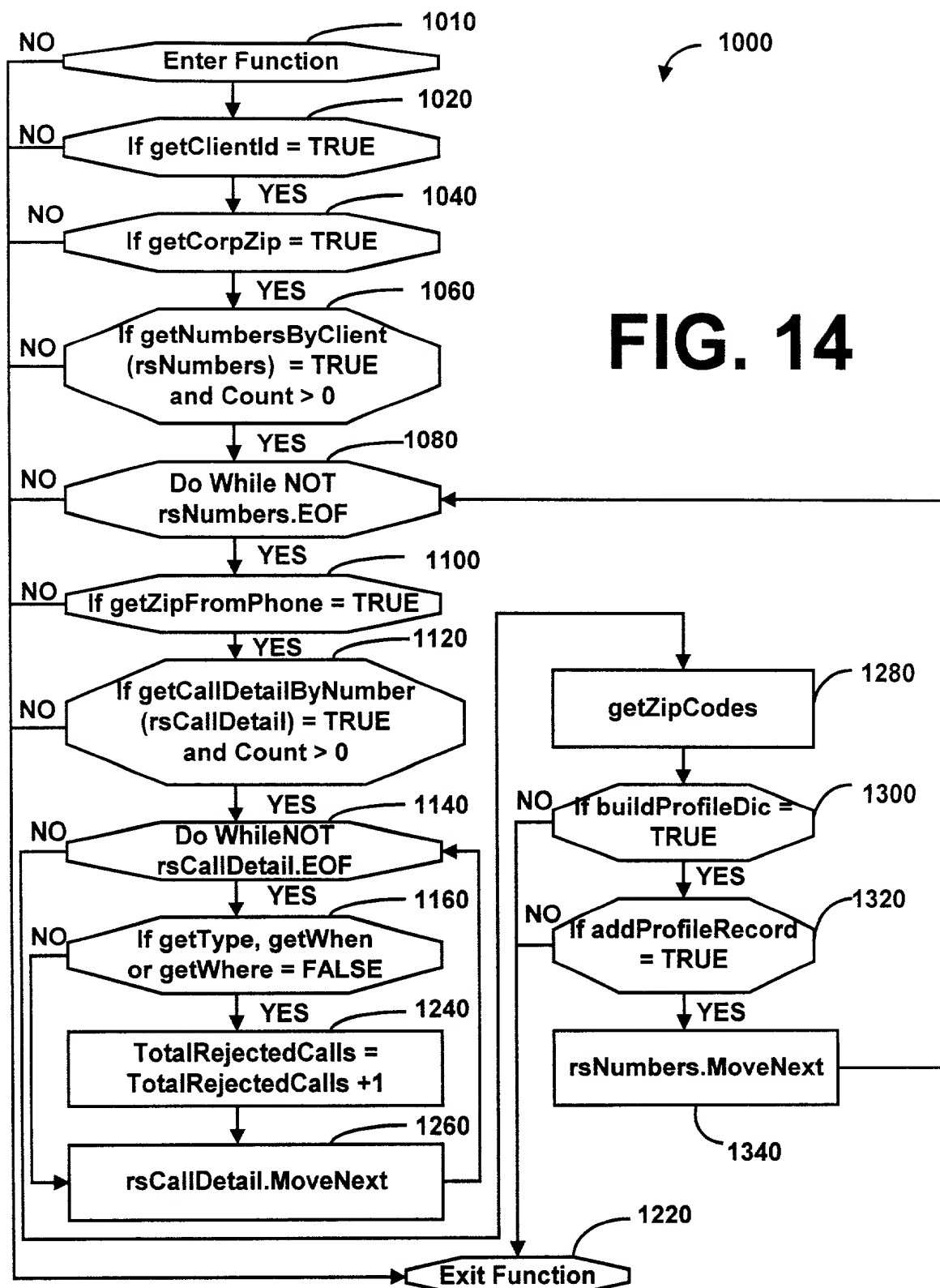
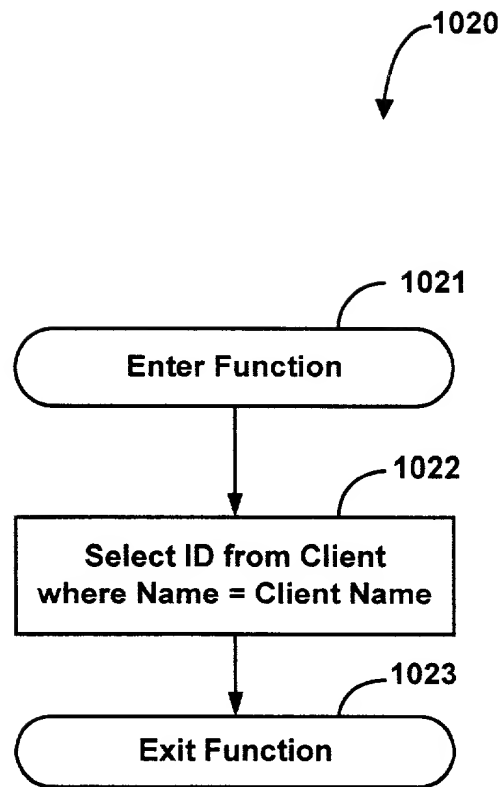
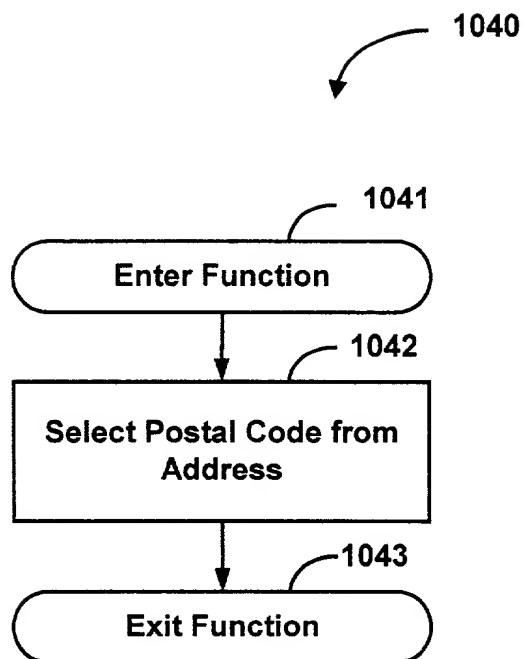


FIG. 14

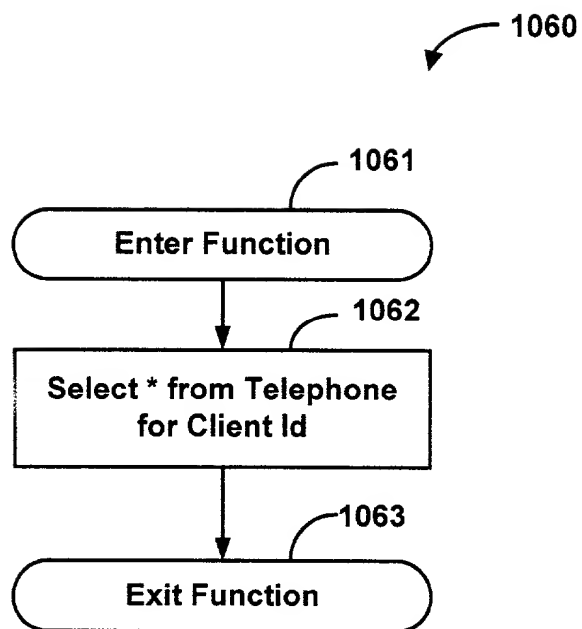


**FIG. 15**

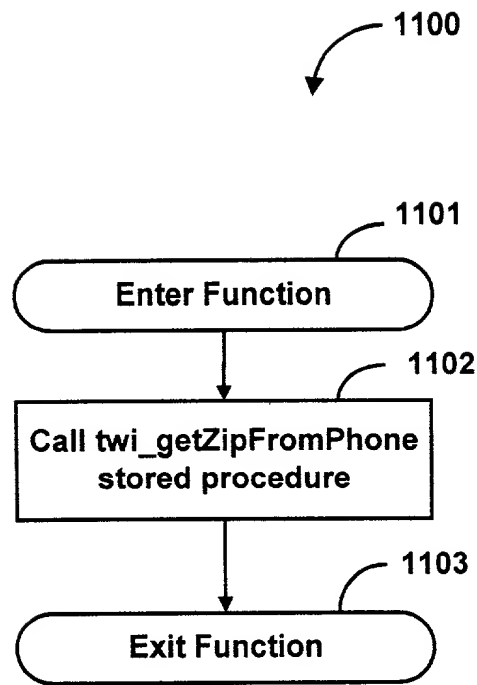




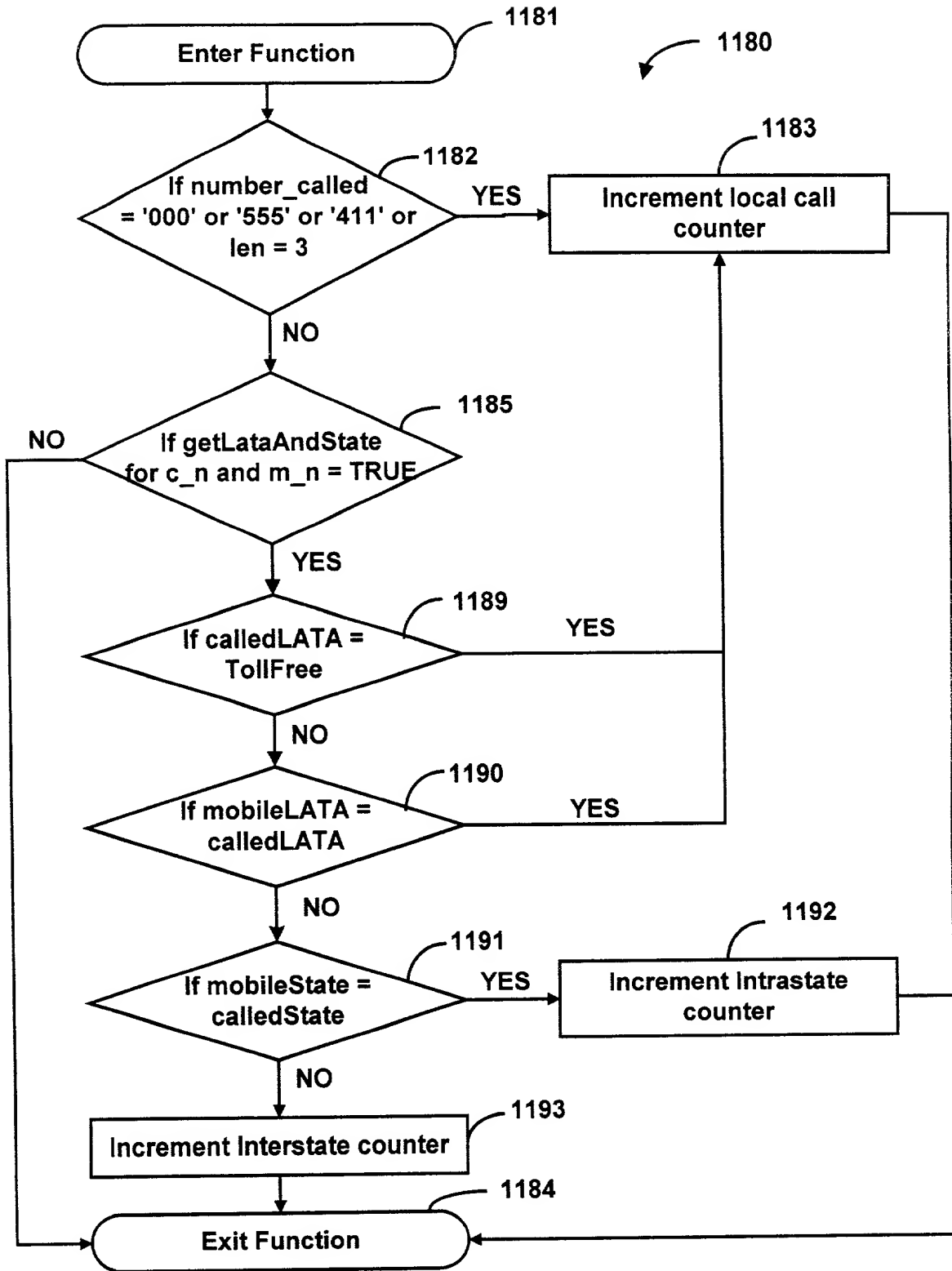
**FIG. 16**



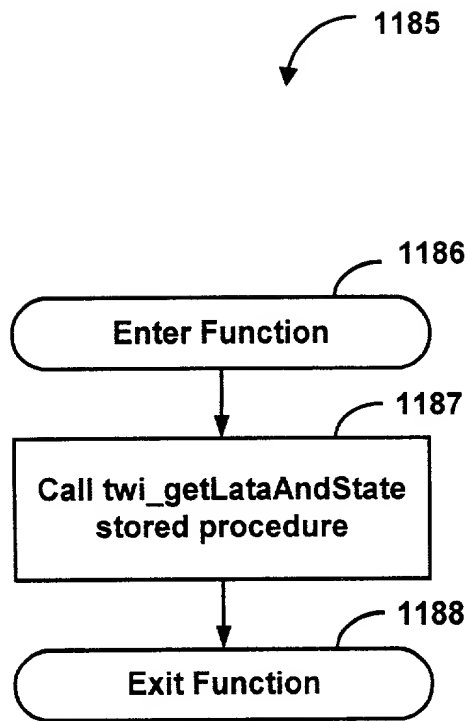
**FIG. 17**



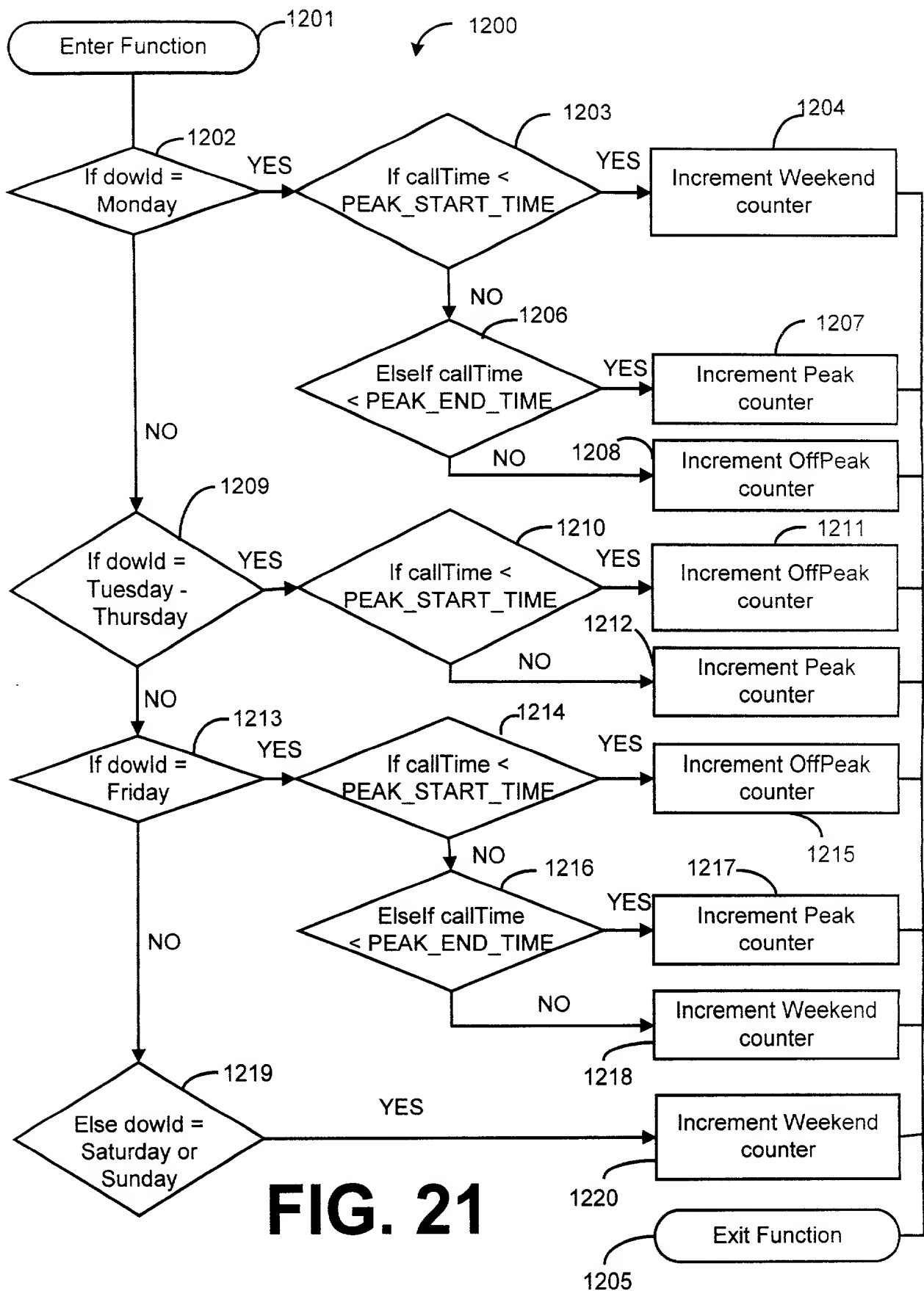
**FIG. 18**

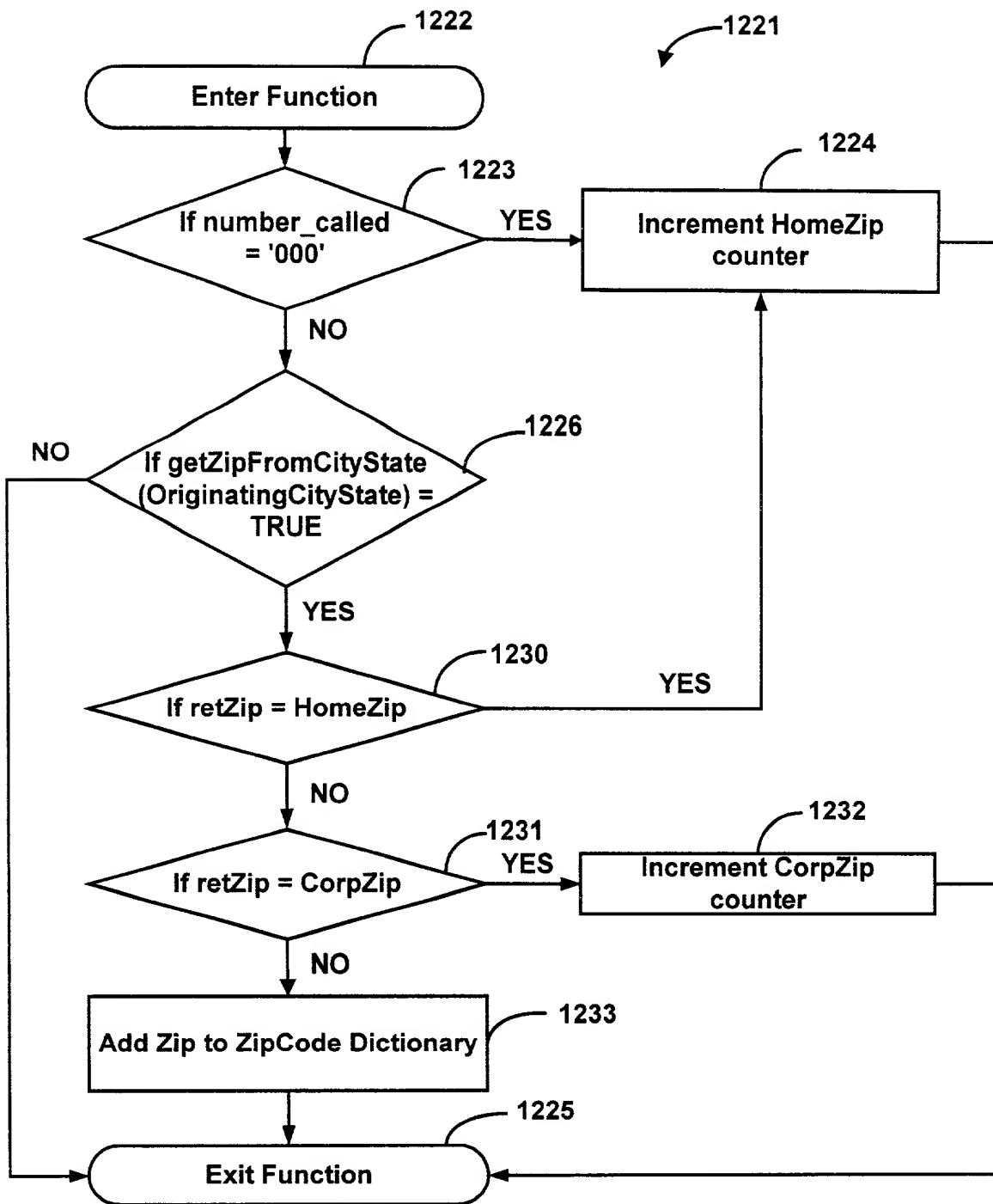


**FIG. 19**

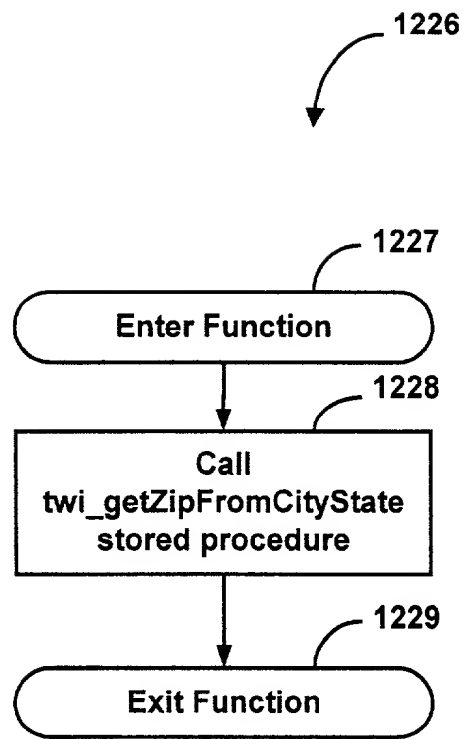


**FIG. 20**



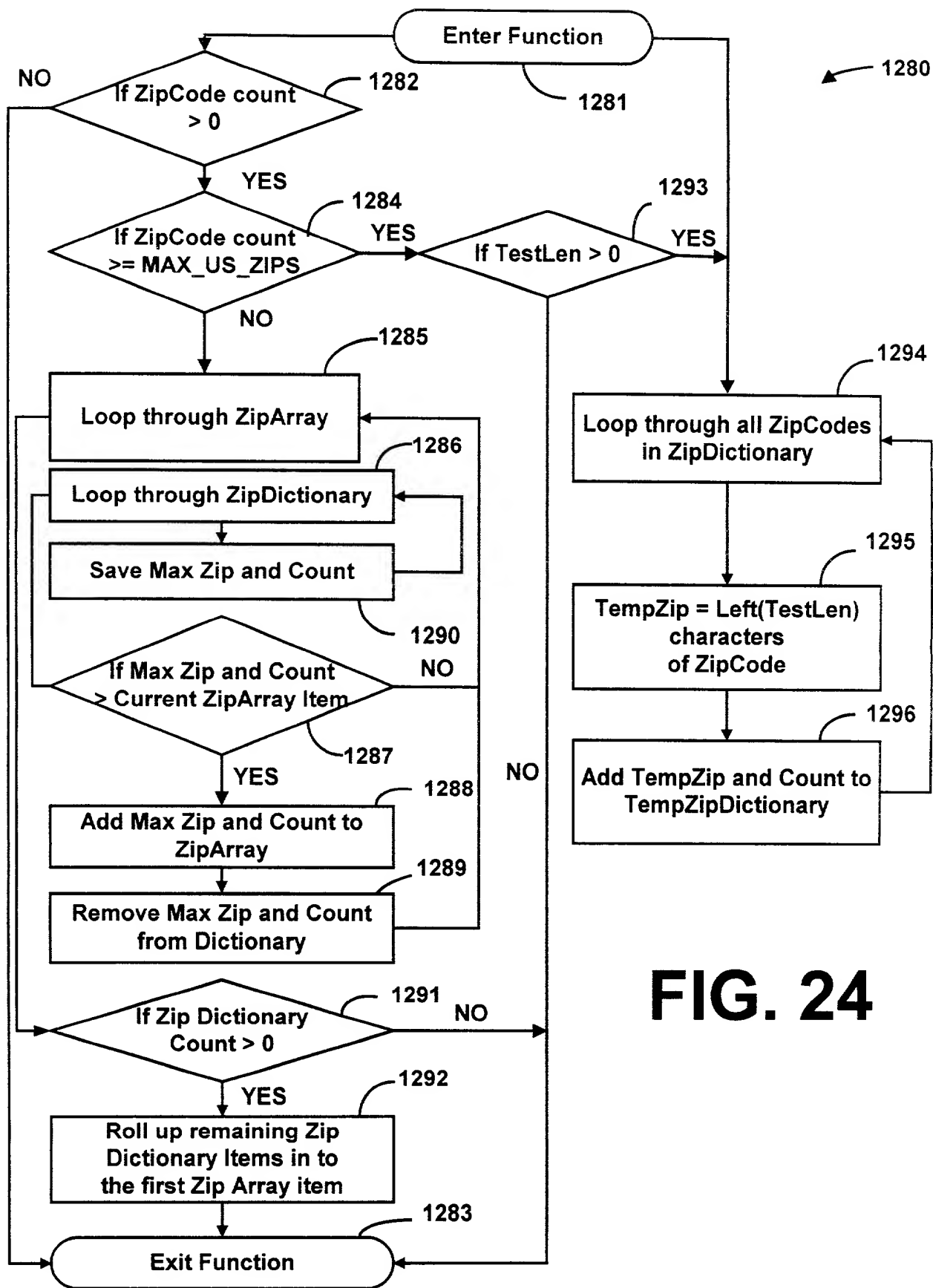


**FIG. 22**

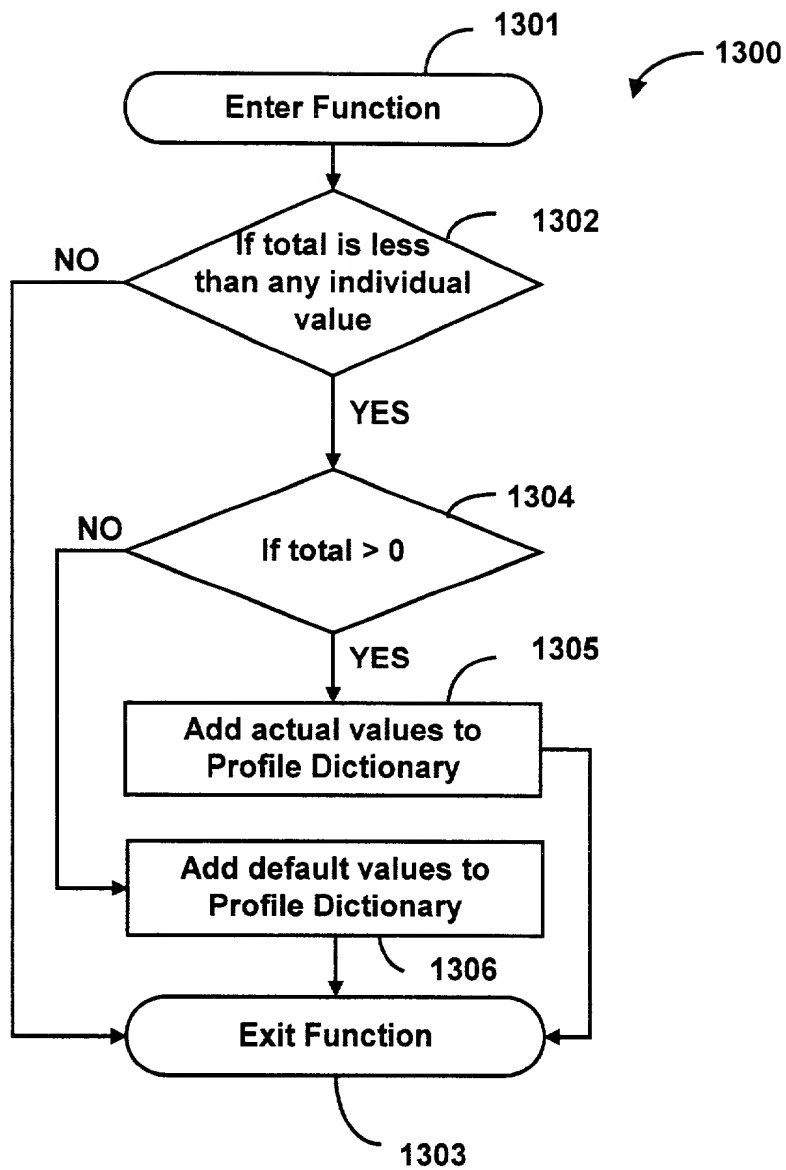


**FIG. 23**

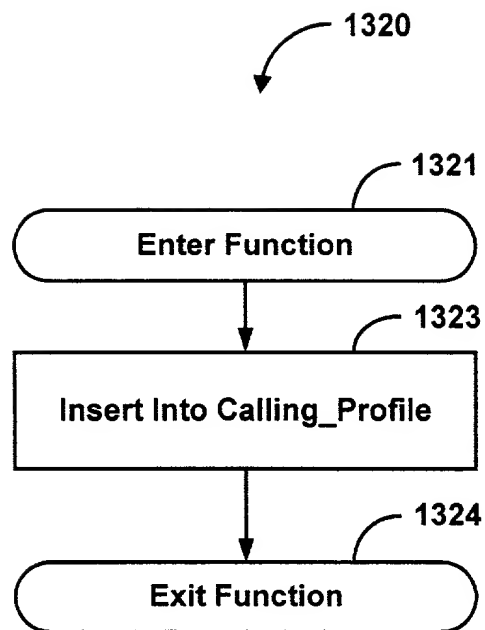




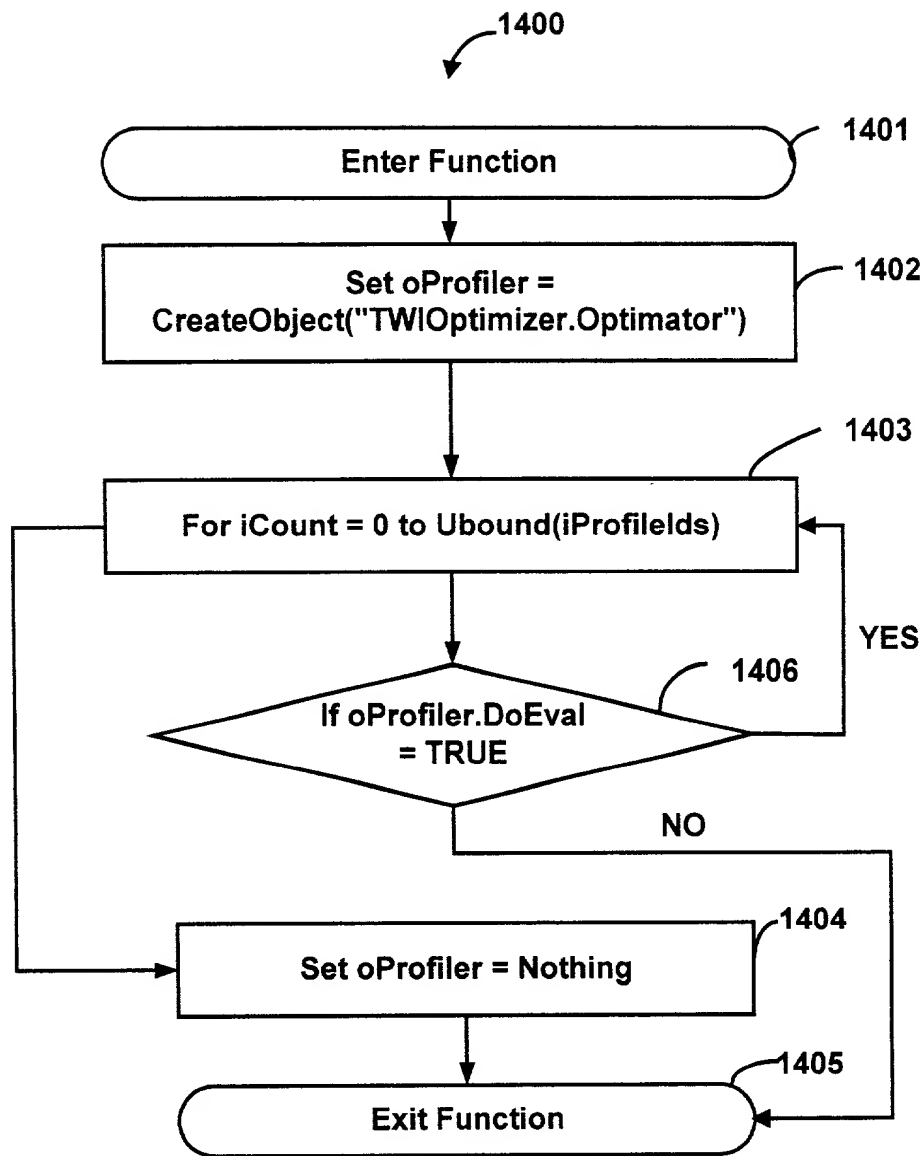
**FIG. 24**



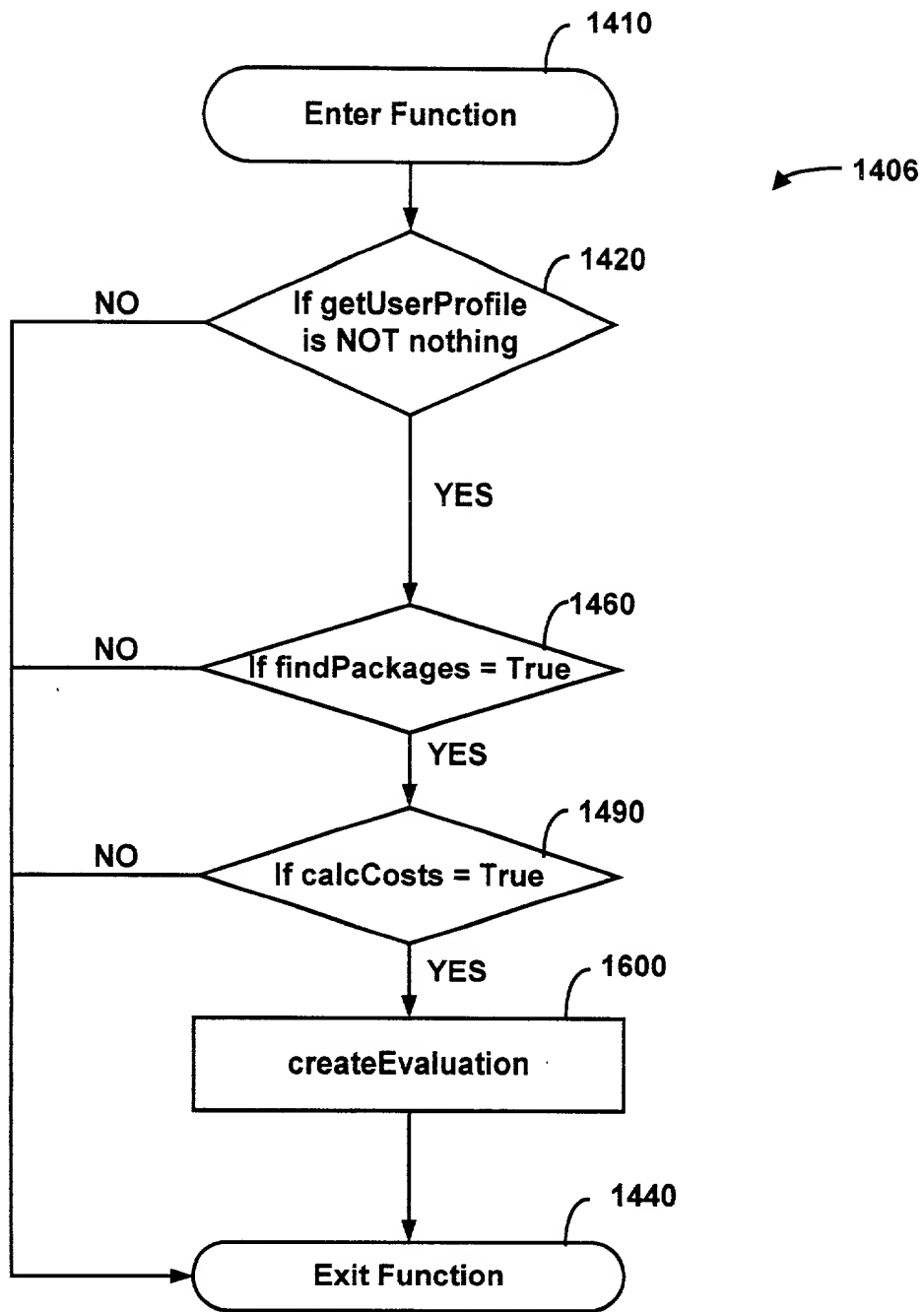
**FIG. 25**



**FIG. 26**

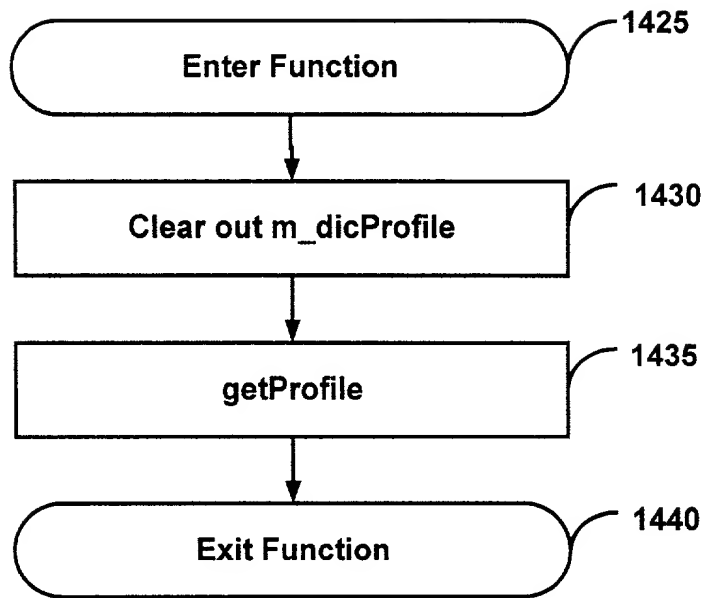


**FIG. 27**

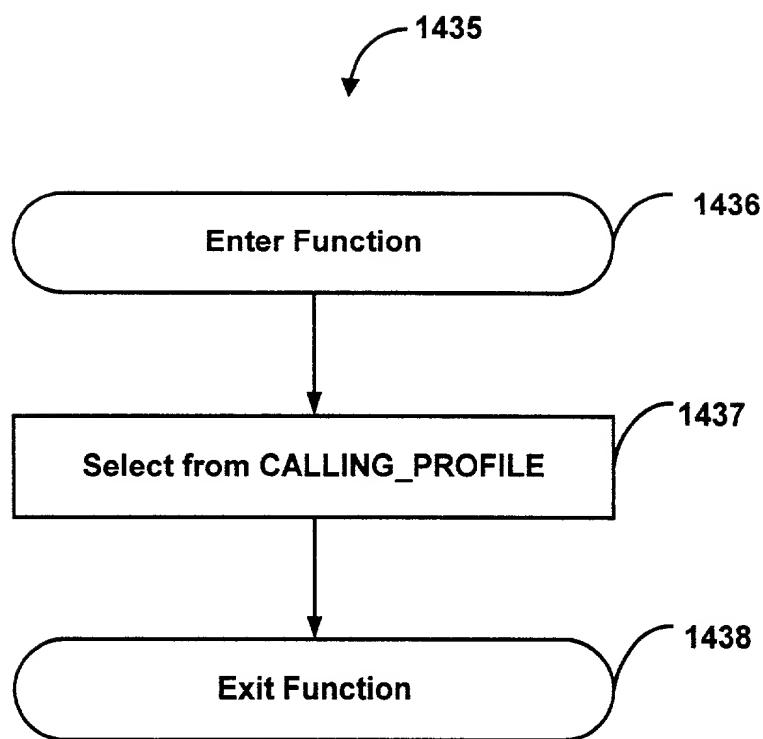


**FIG. 28**

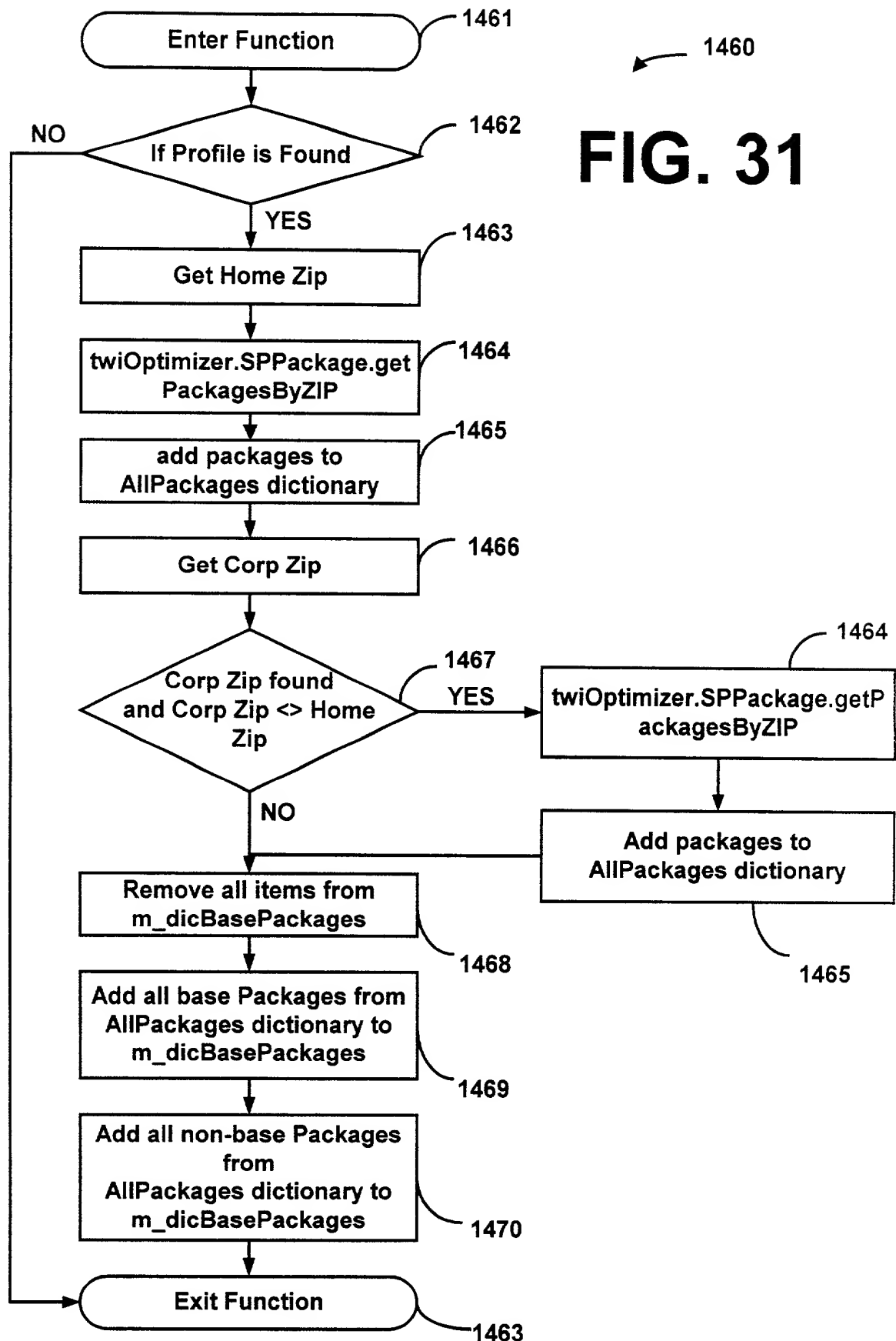
1420



**FIG. 29**

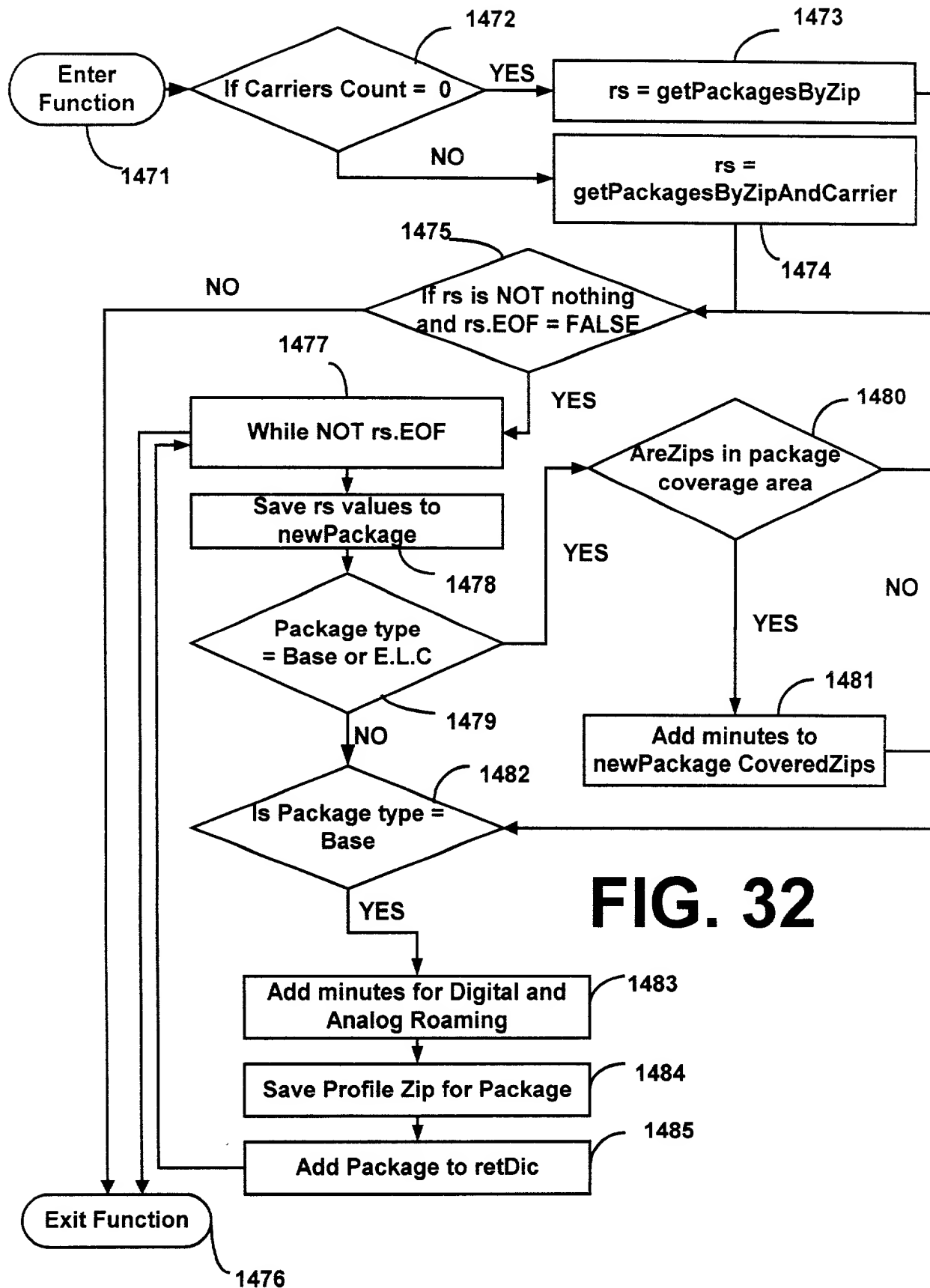


**FIG. 30**

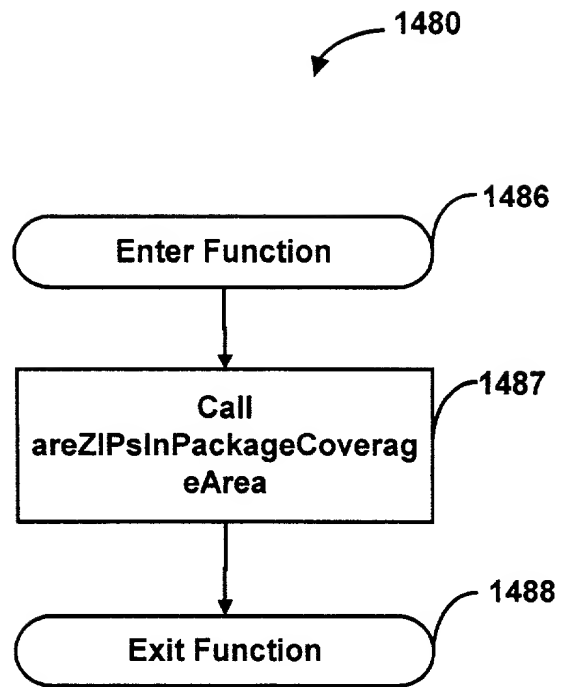


**FIG. 31**

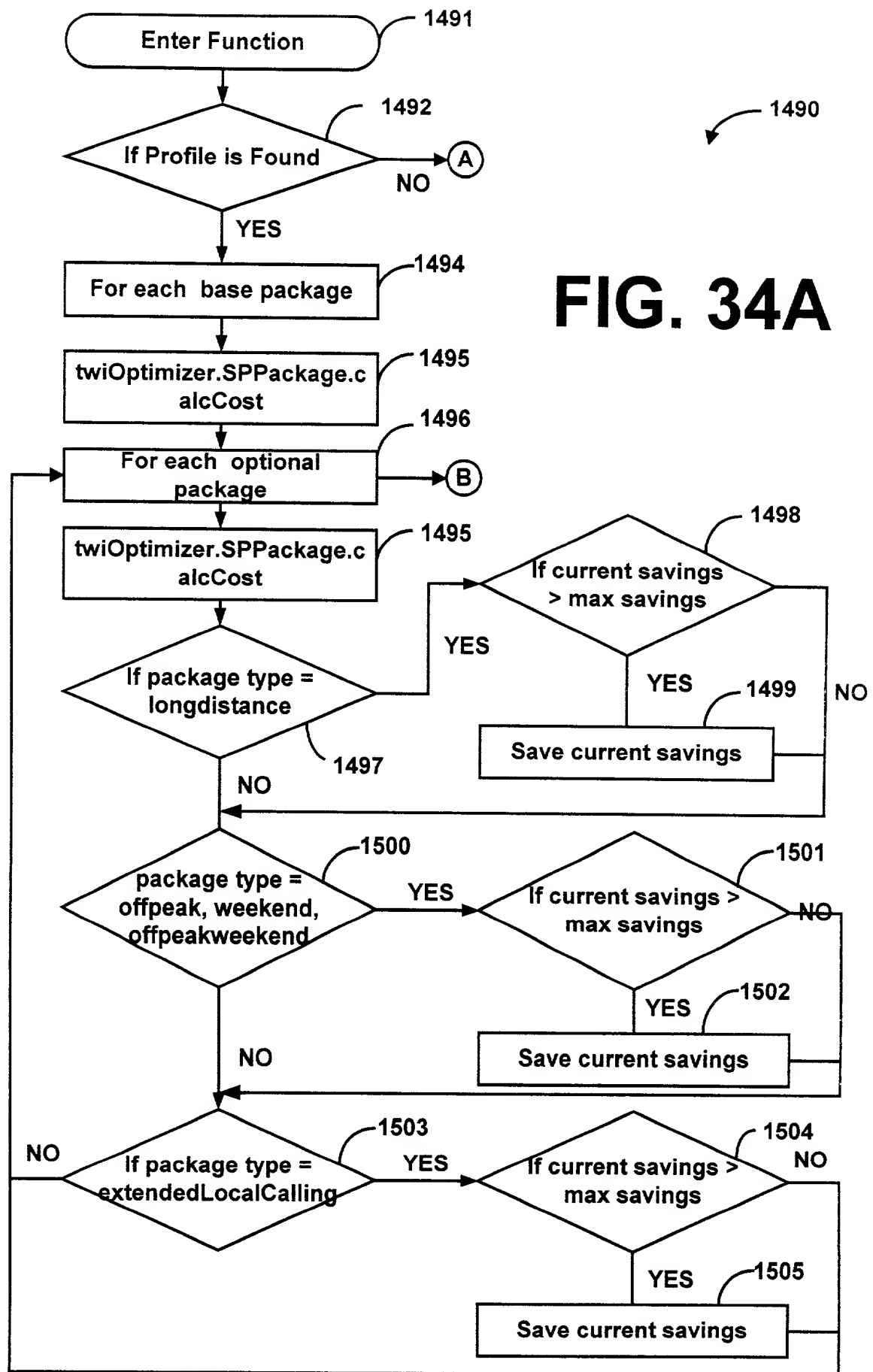




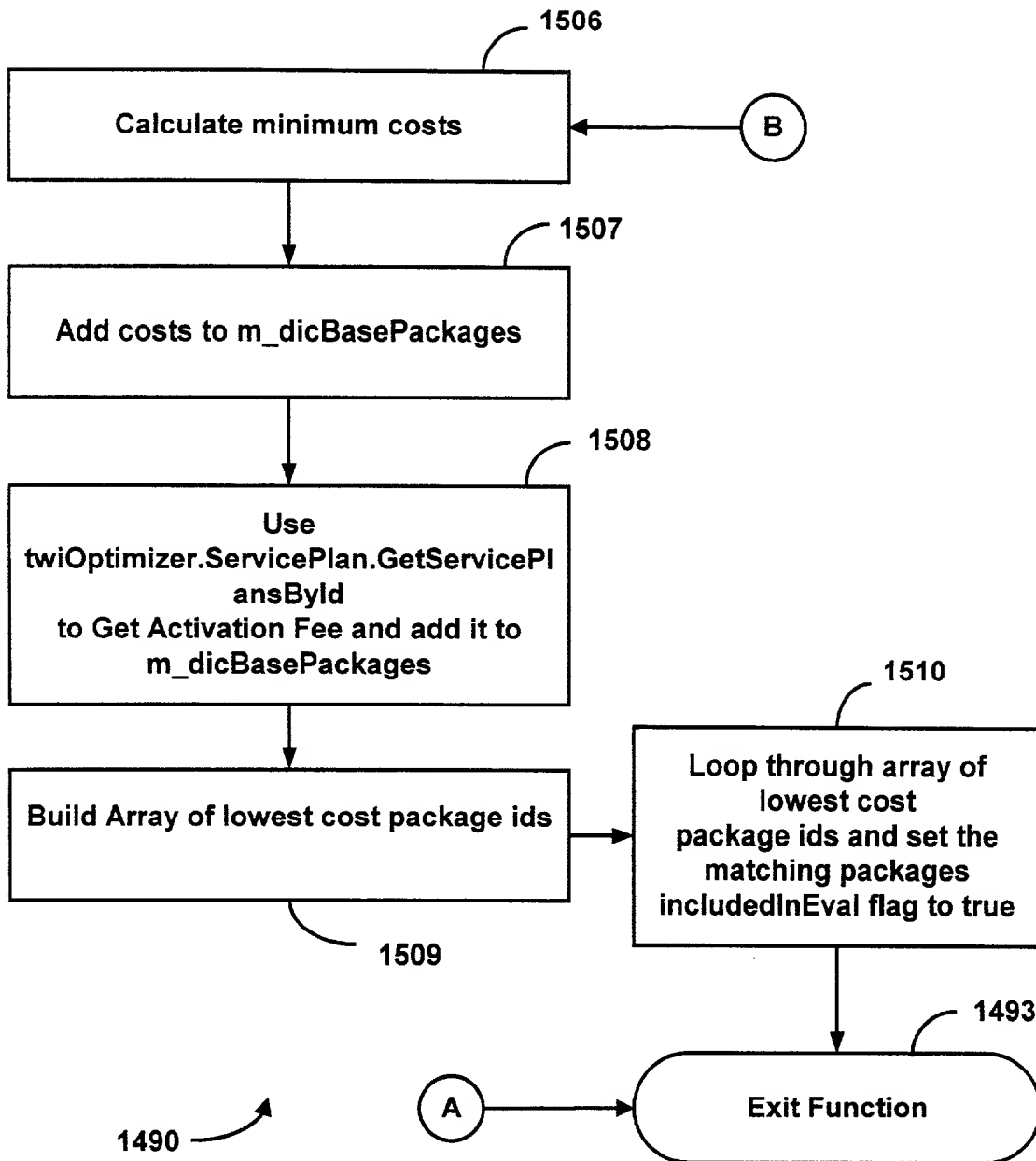
**FIG. 32**



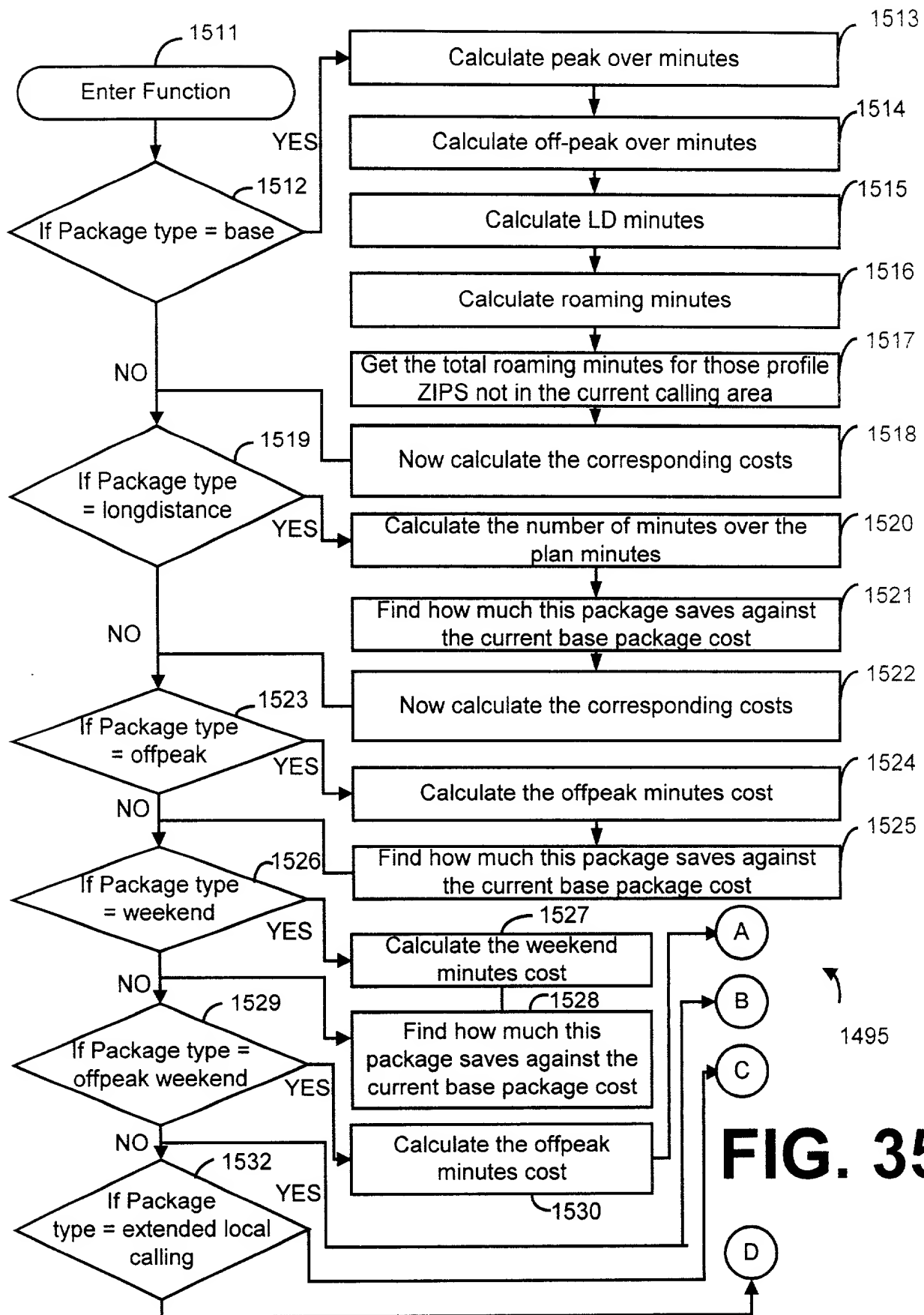
**FIG. 33**



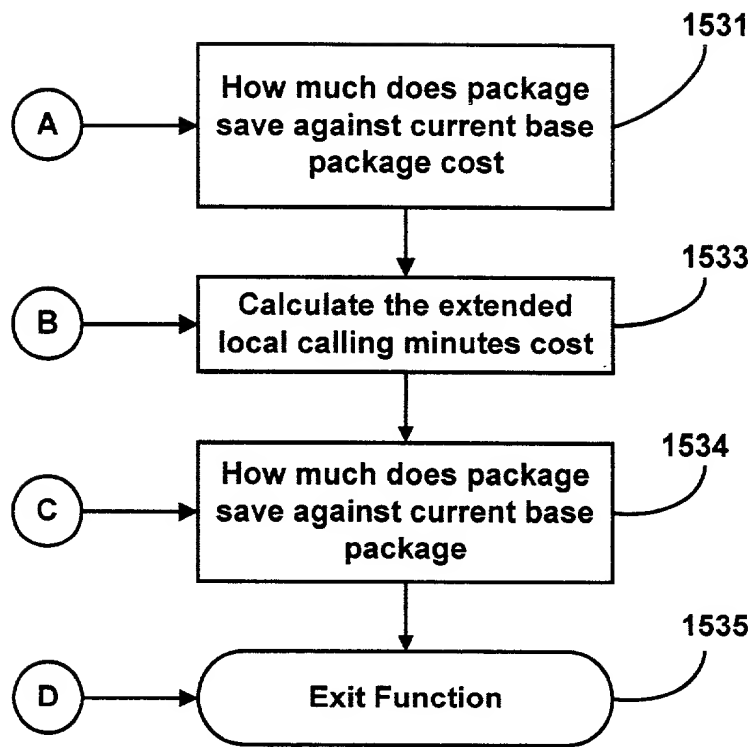
**FIG. 34A**



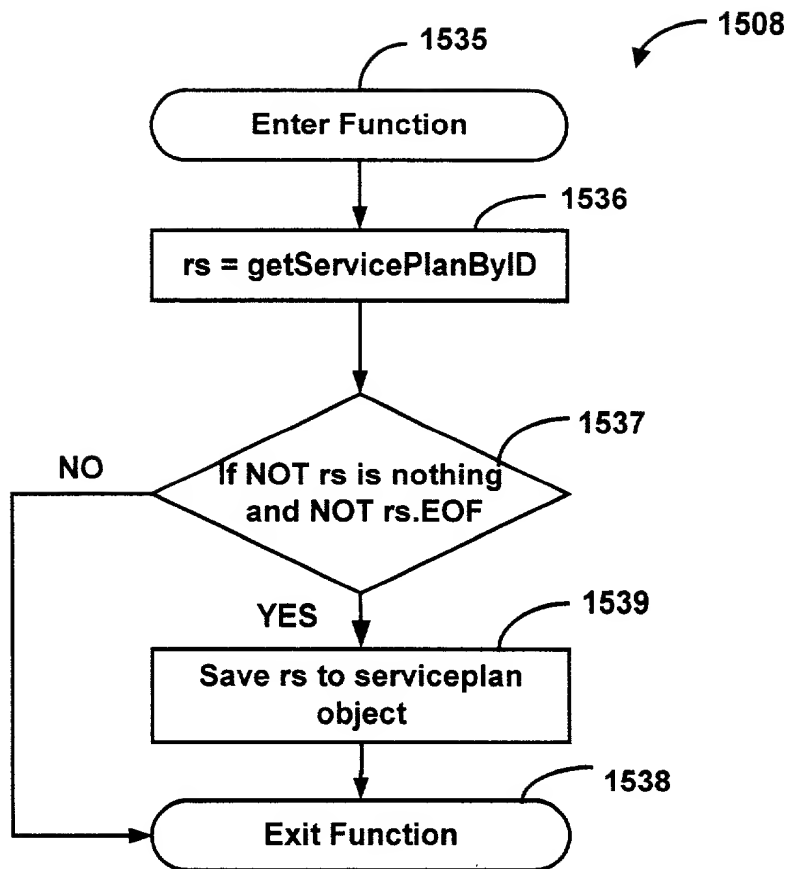
**FIG. 34B**



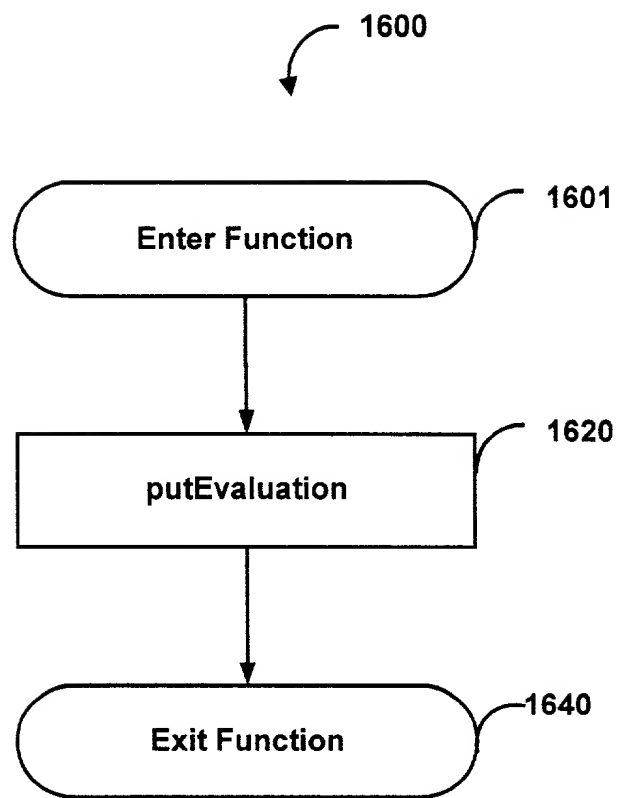
**FIG. 35A**



**FIG. 35B**



**FIG. 36**

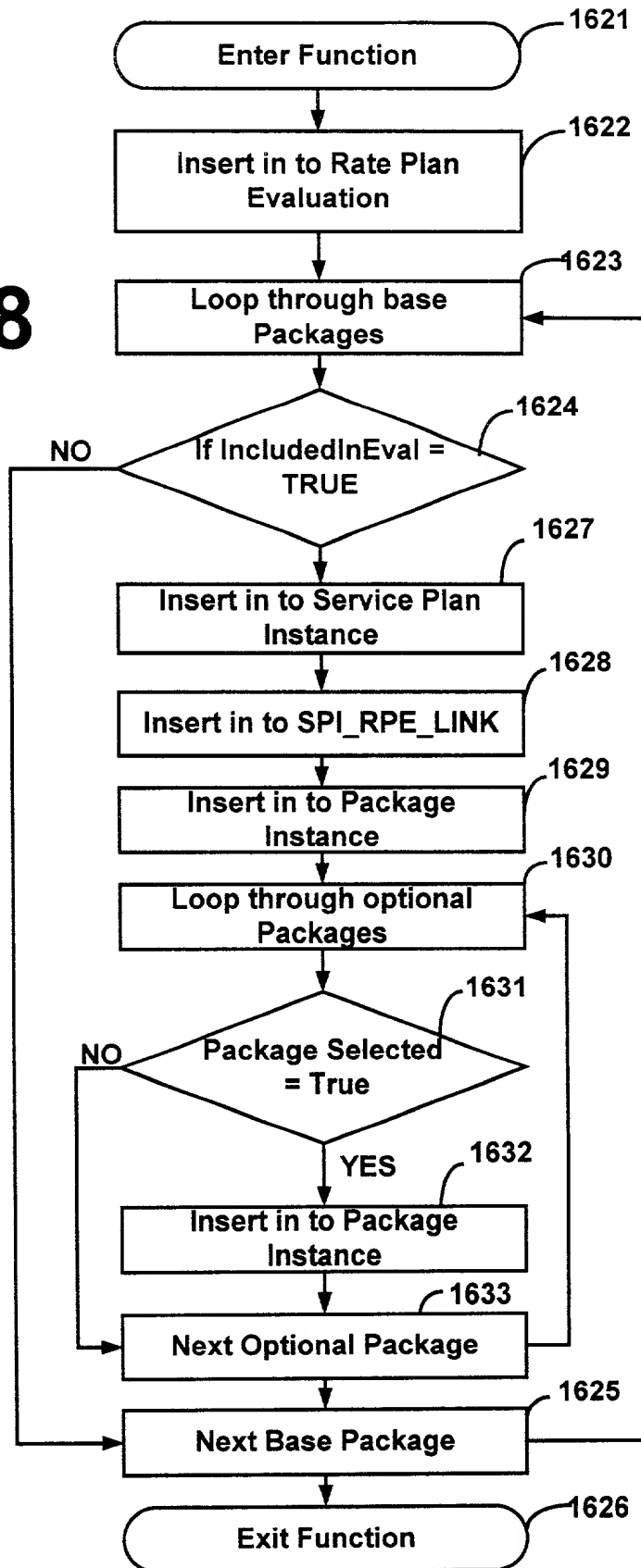


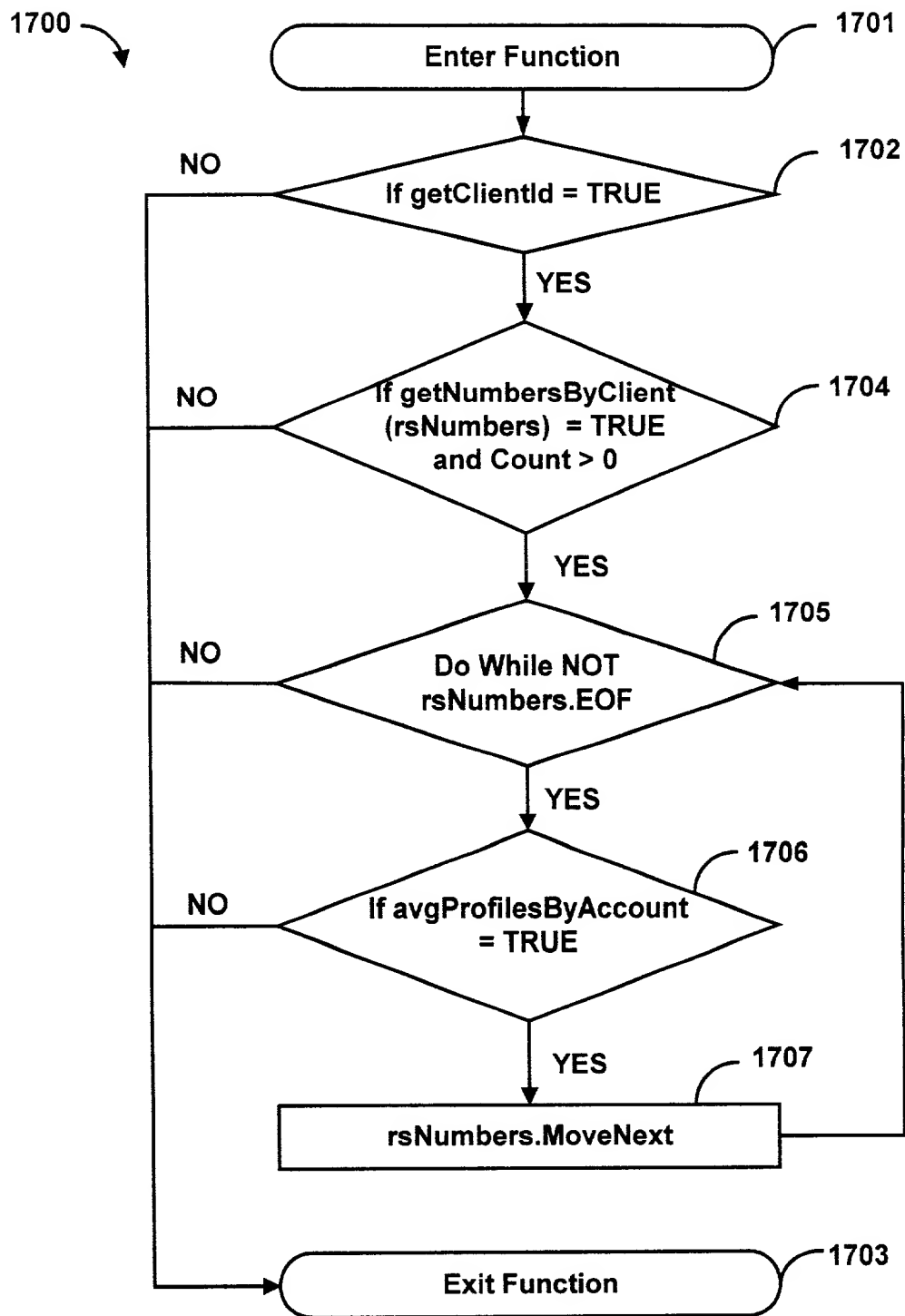
**FIG. 37**



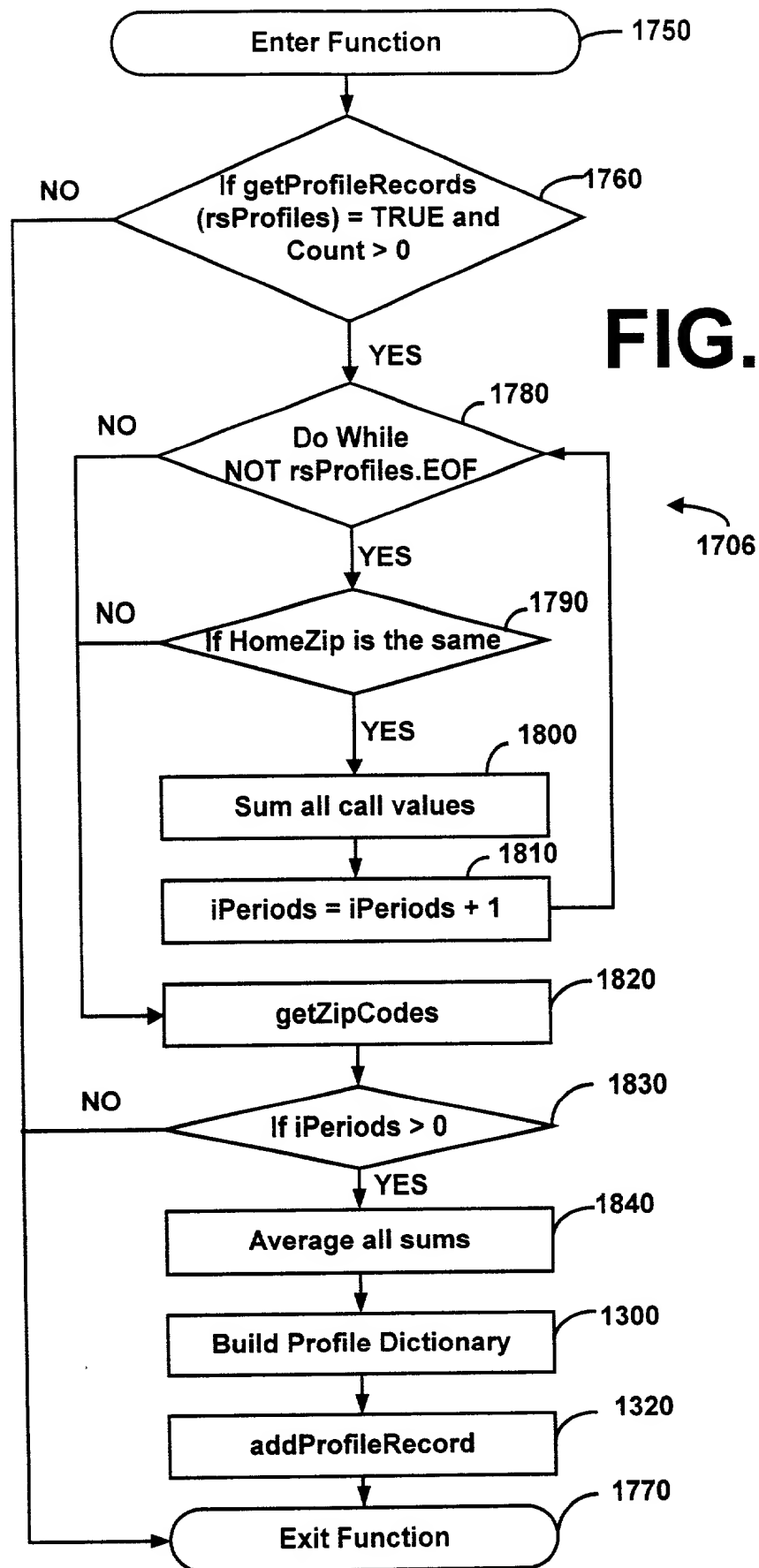
**FIG. 38**

1620 →

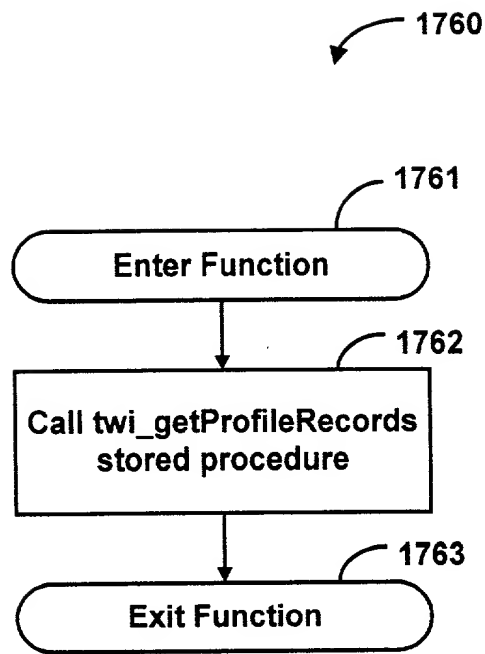




**FIG. 39**



**FIG. 40**



**FIG. 41**